	Specification Symbol Condition / Comment										HTS 80-2000 SCR Unit		
	Maximum Operating Voltage			V _{O(max)}							8 8	kVDC	
	Minimum Operating Voltage Minimum Operating Voltage			V _{O(max)}	ιοπ > 000 μ	ו Case י	70 0				1500	VDC	
	Maximum Isola			V _{O(min)}	Retween H	IV switch an	d control /	GND continue	nuslv		± 15	kVDC	
	Max. Housing Insulation Voltage			V _{INS}		Between HV switch and control / GND, continuously Between switch and housing surface, 3 minutes					± 10	kVDC	
6	Maximum Turn-On Peak Current			I _{P(max)}	T _{case} =	t₀< 200 µs					20000		
RATINGS				-i (iliax)	25°C	t _p < 1 ms, c					10000	ADC	
1						t _p < 10 ms,					7700		
Z				t _p < 100 ms, duty cycle <1%						3600			
2	Max. Non-Repe	I _{p(nr)}	T _{case} = Half sine single pulse, tp<200 μs						20000	ADC			
3	·				25°C Half sine single pulse, tp<20 μs						40000		
MAXIMUM	Max. Coutinuous Load Current				T _{case} = 25°C						5.4	ADC	
Ž	Max. Rate-of-Rise of OFF-State Voltage			dv/dt							125	kV/ μs	
1	I viax. I tale-of-tale of of 1 -State voltage			uv/ut	@ V _{O(max)} , exponential waveform						123	κν/ μs	
ABSOLUTE	Max. Continuous Power Dissipation			P _{d(max)}		Standard of	levices & F	C, forced air 4	m/s		26	Watt	
20	·				T _{case} = 25°C Opt. CF- Copper cooling Fins, forced air 4 m/s						260		
48	Linear Derating				Above 25°C Standard devices & FC, forced air 4 m/s Opt. CF- Copper cooling Fins, forced air 4 m/s						0.58	W/K	
											5.77		
	Operating Temperature Range			To	Standard devices & options CF, GCF, ILC. (Option DLC)						-4075	C°	
	Storage Temperature Range			Ts	Switches with option ILC may require frost protection!						-5090	C°	
	Max. Permissible Magnetic Field Max. Auxilliary Voltage			В	Homogeneous steady-field, surrounding the whole switch				le switch		25	mT	
	Permissible Operating Voltage Range			Vaux	Built-in overvoltage limiter (replaceable)						5	VDC kVDC	
	Typical Breakdown Voltage			V _o	NOTE: V _{br} is a test parameter for quality						0 ± 8		
	•			V br		control purposes only. Not applicable in Ioff > 0.5					>8.8	kVDC	
	Typical Off-State Current			l _{off}	0.8xV ₀ , T _{cs}	_{ase} =2570°	C, reduced	ed I _{off} on request			< 600	μADC	
	Typical Holding Current							Tcase=70°C			35	mADC	
	Typical On-State Voltage			V_{sat}	t _p < 1μs, duty cycle < 1%			0.01 x I _{P(max)}			150		
								0.1 x I _{P(max)}			310		
	Tarical December 2 Dalor Time							1.0 x I _{P(max)}			830	VDC	
	Typical Propagation Delay Time		t _{d(on)}	Resistive load, 0.1 x I _{P(max)} , 0.8 x						0.4	μs		
S	Typical Output Pulse Jitter Typical Turn-On Rise Time			t _j	Impedance matched input, V _{aux} / V Resistive load, 10-90%						TBD.	ns	
12	Typical fulli-Off Rise fillie			ur(on)	0.8 x V _{O(max)} , l _L = 0.1 x l _{pl} 0.8 x V _{O(max)} , l _L = 0.1 x l _{pl} 0.8 x V _{O(max)} , l _L = 1.0 x l _{pl}				$1.1 \times V_{O(max)}$, $I_L = 0.1 \times I_{p(max)}$		IDD.		
ISI											ns		
ER	Typical Turn-Off Time			t _{off,} t _q	Resistive I	oad, 10-90%)	0.1 x V _{O(max)}			35	ns	
2					$0.8 \times V_{O(max)}$, $I_L = 1.0 \times I_{p(max)}$				90				
3	On Time			ton							35∞	ns	
CHARACTERISTICS	Internal Driver Recovery Time			t _{rc}							500	ns	
	Max. Continuous Switching			f _(max)	@ V _{aux} = 5.	/ _{aux} = 5.00 V Standard devices without HFS option							
RICAL	Frequency				Sw. shutdown i	f f _(max) is		devices with		,	TBD.		
	Mariana Barat Francisco				exceeded	I				on		kHz	
ELECT	Maximum Burst Frequency			f _{b(max)}	Use option HFB for >10 pulses within 20µs or less						10	kHz	
EL	Maximum Number of Pulses / Burst Coupling Capacitance			$N_{(max)}$	@ f _{b(max)} Standard					IED	15 Use option HFB for >15	Pulses	
					Note: Option HF	B requires externa	requires external buffer capacitors with a voltage C and a cpacitance of 100nF per additional Option HFB				>100 >1000		
				Сс				additional	Орионтн		>280	pF	
	Control Voltage Range			V _{ctrl}	HV side against control side The V _{ctrl} has no impact on the output pulse shape.				ipe.		4 5	VDC	
	Auxiliary Supply Voltage Range			Vaux	The +5 V supply is not required in the HFS mode.						5	VDC	
	Typical Auxiliary Supply Current			I _{aux}	$V_{aux} = 5.00 \text{ VDC}, T_{case} = 25^{\circ}\text{C}.$ 0.01 x f _(max)					ax)	TBD.		
	3			Active curre	ent limitation a	above 1A.		@ f _(max)	,	600	mADC		
	Fault Signal Output			Switch will be turn off, if f>f(max), Vaux<4.75V or Tcase>75°C						H=4V, L=0.5V	VDC		
				Fault condition is indicated by a logical "L"									
	Trigger Voltage Range			V_{TR}	Switching behaviour is not influenced by trigger quality						<10	VDC	
45	Dimensions			LxWxH	· ·						Please contact the		
HOUSING						Devices with option CF, non-isolated cooling fins					manufactured!	mm ³	
	Weight				Devices with option DLC Standard housing							1	
2	vveigiit				Devices with option CF, non-isolated cooling fins						Please contact the	α .	
					Devices with option DLC						manufactured!	g	
	Control Signal	Pin 1 / Yellov		tible with Schmitt-Trigger characteristics. Control voltage 2-10 V (3-5 V recommended for low jitter).									
	Logic GND / 5V Return Pin 2 / Black. The ground pin is internally connected with the safet								_				
VS	5V Auxiliary Supply Pin 3 / Red. The 5 V input is used for rep rates up to the specified max. frequency f _(max) . In								,	,			
0	Fault Signal Output Pin 4 / Orange. TTL output, short circuit proof. Indicating switch & driver over-heat, over-							_					
C1							nitt-Trigger characteristics for the connection of external safety circuits. L = Switch Inhibited.						
FUNCTIONS	LED Indicators GREEN: "Auxiliary power good, switch OFF". YELLOW: "Control signal received, switch								•				
•					-			-			ne < 60 s @ 3xPd(max), ∆T=25K (50 to 75°C). Separate driver		
	. oporaturo i							5°C), coolant flow > 3l / min. Separate driver protection.	. ••				
	HTS 80-2000 SCR	Thyristor Swit	ch, 8 kVDC, 2000 Al		ion LP Low Pass. Input filter for increased noise immunity. Option					Option CCS	CCS Ceramic Cooling Surface. P _{d(max)} can be increased by the factor 2 to 3.		
		,			Option S-TT Soft Transition Time. Slower switch			ning speed for simplified EMC. Option (CCF Ceramic Flange Housing. P _{d(max)} can be increased by the factor 3 to 15.		
111										Ontion CE	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,		
WGTI				Opti						_		to 15	
ERINGTI				Opti Opti	on HFS High	h Frequency Sw	tching (two au		s V1 & V2)	Option GCF Option ILC	Copper Cooling Fins: P _{d(max)} can be increased by the factor 3 to 10. Grounded Cooling Flange (copper). P _{d(max)} can be increased by the factor 3 Indirect Liquid Cooling (for water). P _{d(max)} can be increased by the factor 3 to		
RDERINGTI				Opti Opti Opti	on HFS High on UFTR Ultra	h Frequency Sw a Fast Thermotri	tching (two au	xiliary supply input	s V1 & V2) wn < 5s.	Option GCF	Grounded Cooling Flange (copper). P _{d(max)} can be increased by the factor 3	15.	
ORDERINGTI	omized switching u			Opti Opti Opti Opti	on HFS High on UFTR Ultra on UFTS Ultra FOR	h Frequency Sw a Fast Thermotri a Fast Thermose t FURTHER PR	tching (two au gger. Respons ensor. Respon ODUCT OPTI	xiliary supply input se time for shut do se time < 5s. NTC ONS PLEASE RE	s V1 & V2) wn < 5s. 10k / ± 1% FER TO THE	Option GCF Option ILC Option DLC OPTIONS PA	Grounded Cooling Flange (copper). P _{d(max)} can be increased by the factor 3 Indirect Liquid Cooling (for water). P _{d(max)} can be increased by the factor 3 to Direct Liquid Cooling (for FPE/PFC). P _{d(max)} can be increased by the factor 1 GE.	15. 10 to 100. 15.	