	Specification	Symbol	Condition / Cor					HTS 701-30 SiC	Unit	
	Maximum Operating Voltage	$V_{O(max)}$	I _{off} < 50 μADC, T _{case} = 70°C					70	kVDC	
	Maximum Isolation Voltage	Vı	Between HV switch and control / GND, continuously					± 80	kVDC	
RATINGS	Max. Housing Insulation Voltage	V _{INS}		Between switch and housing surface, 3 minutes		10/		± 110	kVDC	
	Maximum Turn-On Peak Current	I _{P(max)}	T _{case} = 25°C		t_p < 200 µs, duty cycle <1%			300		
2					t _p < 1 ms, duty cycle <1% t _p < 10 ms, duty cycle <1%			190 58		
_					t _o < 100 ms, duty cycle <1%			30	ADC	
MAXIMUM	Maximum Continuous Load Current	I _{L(max)}			Standard devices			2.52	+	
×	12(1100)		T _{case} = 25°C		Devices with option DLC			60	ADC	
M	Max. Continuous Power Dissipation	P _{d(max)}	T _{case} = 25°C		Standard devices & FC, forced air 4 m/s			45		
UTE			T _{case} - 23 C		Devices with option D			4500	Watt	
77	Linear Derating		Above 25°C		Standard devices & F			0.2	14404	
BSOL	Operating Temperature Dance	T	Devices with option DLC Standard devices & options CF, GCF, ILC. (Option DLC)					75 -4060	W/K °C	
AB	Operating Temperature Range Storage Temperature Range	T ₀	Switches with option ILC may require frost protection!					-4060 -4090	°C	
	Max. Permissible Magnetic Field B		Homogeneous steady-field, surrounding the whole switch					25	mT	
	Max. Auxilliary Voltage V _{aux}		Built-in overvoltage limiter (replaceable)					5.5	VDC	
TERISTICS	Permissible Operating Voltage Range V ₀							0 ± 70	kVDC	
	Typical Breakdown Voltage V _{br}		NOTE: V _{br} is a test parameter for quality control purposes only. Not applicable in normal operation!			77	kVDC			
	Typical Off-State Current Ioff		0.8xV ₀ , T _{case} =2570°C, reduced l _{off} on request			<80	μADC			
	Typical Turn-On Resistance R _{sta}				0.1 x I _{P(max)} , T _{case} =2	25°C	1.2	p/ ID 0		
			t _p < 1µs, duty cycle < 1%			1.0 x I _{P(max)} , T _{case} =25°C		2.9		
			$1.0 \times I_{P(max)}, T_{case} = 70^{\circ}C$				′0°C	4.9	Ohm	
	Typical Propagation Delay Time	Resistive load, 0.1 x I _{P(max)} , 0.8 x V _{O(max)} , 50-50%					200	ns		
	Typical Output Pulse Jitter	tj			$V_{aux} / V_{ctrl} = 5.00 VDC$	104 1/ 1 0	4 1	3	ns	
	Typical Turn-On Rise Time	t _{r(on)}	Resistive load, 10-90%			$0.1 \times V_{O(max)}, I_L = 0.1 \times I_{p(max)}$		TBD TBD		
						0.8 x $V_{O(max)}$, $I_L = 0.1$ x $I_{p(max)}$ 0.8 x $V_{O(max)}$, $I_L = 1.0$ x $I_{p(max)}$		TBD	ns	
	Typical Turn-Off Rise Time	t _{off} , t _q	Resistive load, 10-90%			$0.0 \times V_{O(max)}$, $I_L = 1.0 \times I_{p(max)}$ $0.1 \times V_{O(max)}$, $I_L = 0.1 \times I_{p(max)}$		TBD	1	
			resistive issue, 10 55%			0.8 x V _{O(max)} , I _L = 1.0		TBD	ns	
	Maximum Turn-On Time	t _{on(max)}	No limitation					∞	ns	
	Minimum Turn-On Time	t _{on(min)}	t _{on(min)} can be customized. Please consult factory					250	ns	
<u>C</u> 1	Maximum Turn-Off Time	t _{off(max)}	No limitation					∞	ns	
CHARA	Minimum Turn-Off Time	t _{off(min)}	toff(min) can be customized. Please consult factory					250	ns	
	Max. Continuous Switching	f _(max)	@ V _{aux} = 5.00 V Sw. shutdown if f _{med} is Standard devices without HFS option Standard devices with HFS supply					TBD		
-	Frequency		Sw. shutdown if f _(max) is Standard devices with HFS supply Opt. HFS + sufficient cooling option					80 150	kHz	
ECTRICAL	Maximum Burst Frequency	laximum Burst Frequency f _{b(max)}		or >10 nu	lses within 20µs or les		1	MHz		
18			@ f _{b(max)} Standard					>100	+	
FC			Note: Option HFB requires external buffer capacitors with a votage rating of > 630/DC and a capacitance of 100rF per additional pulse. Ontion HFR					>1000		
THE ET			Орионти В					>10000	Pulses	
	Coupling Capacitance Cc Natural Capacitance C _N		HV side against control side Between switch poles, @ 0.5 x V _{O(max)}					<100 <10	pF pF	
	Control Voltage Range V _{ctrl}		The V _{ctrl} has no impact on the output pulse shape.					3 10	VDC	
	Auxiliary Supply Voltage Range	The +5 V supply is not required in the HFS mode.					4.5 5.5	VDC		
	Typical Auxiliary Supply Current	$V_{aux} = 5.00 \text{ VDC}, T_{case} = 25^{\circ}\text{C}.$ 0.01 x f _(max)					TBD	1		
			Active current limitation above 1A. @ f _(max)					800	mADC	
	Fault Signal Output		Switch will be turn off, if f>f _(max) , V _{aux} <4.75V or T _{case} >75°C Fault condition is indicated by a logical "L"					>4.0 <0.8	VDC	
	Opt. HFS, Ext. Supply Voltage V1	Stability ±3%, current consumption <0.4 mA/kHz @ 25°C					15	VDC		
	Opt. HFS, Ext. Supply Voltage V2	Stability ±3%, current consumption <0.4 mA/kHz @ 25 °C					TBD	VDC		
	Intrinsic Diode Forward Voltage	T _{case} = 25°C, I _F = 0.3 x I _{P(max)}					130	VDC		
	Diode Reverse Recovery Time	$T_{case} = 25$ °C, $I_F = 0.3 \text{ x } I_{P(max)}$, $di/dt = 100 \text{ A/}\mu\text{s}$					<50ns	ns		
10	Dimensions	LxWxH	Standard housing							
HOUSING		Devices with option DLC						TBD	mm ³	
	Weight Standard housing								+	
		Devices with option DLC					TBD	Kg		
	Control Signal Input Pin 1 / Yellow (LS-C: Pin 1). TTL compatible (LS-C: With 100Ω termination). Schmitt-Trigger charact						gor charactor	ictics Control voltage 2 10 V /3 5 V for low ii	ttor)	
FUNCTIONS	Logic GND / 5V Return Pin 1 / Yeilow (LS-C: Pin 1). TTL compatible (LS-C: With 100Ω termination). Schmitt-Ingger character Logic GND / 5V Return Pin 2 / Black (LS-C: Shielding). The ground pin is internally connected with the safety earthings termination.								ilei).	
	5V Auxiliary Supply Pin 3 / Red (LS-C: Pin 4). The 5 V input is used for rep rates up to the specified max. frequency f _(max) .									
	Inhibit Signal Input Pin 5 / Green (LS-C: Pin 2). TTL compatible, Schmitt-Trigger characteristics for the connection of extended in the connection of exten									
F	LED Indicators GREEN: "Auxiliary power good, switch OFF". YELLOW: "Control signal received, switch ON". RED							"Fault condition, switch OFF"		
	Temperature Protection Switches with option DLC: 65° C, response time < 3 s @ $3xPd(max)$, $\Delta T=25K$ (40 to 65° C), coolant flow > 31									
ORDERING							Option I-PC			
			Option HFB High Frequency Burst (improved capability by external capacitors) Option UL-94 Option HFS High Frequency Switching (two auxiliary supply inputs V1 & V2) Option I-FWD					and the second great part of the second great		
		Option I-HFS Integrated High Frequency Burst Option I-FWI					N Integrated Freewheeling Diode Network. In connection with inductive load.			
ER		Option S-TT Soft Transition Time decrease the rise and fall time by 20% Option PT-C Option Min-On Individually increased "Min. On-Time" to avoid unwanted triggering Option SEP-I					Pigtail for control connection: Flexible leads (I=75mm) with lemo Separated control unit. Control unit with LED indicators in a separate			
JRD		Option Min-Off Individually increased "Min. Off-Time" to avoid unwanted triggering Option TH					Tubular Housing			
0		Option PCC Pulser Configuration. Switch combined with custom specific parts. Option CF					Copper Cooling Fins. P _{d(max)} can be increased by the factor 3 to 10.			
			Option ISO-120 120kV Isolation. Isolation Voltage increased to 120kV. Option DLC Option ISO-200 200kV Isolation. Isolation Voltage increased to 200kV. FOR FURTH				Direct Liquid Cooling. Pq(max) can be increased by the factor 1 R PRODUCT OPTIONS PLEASE REFER TO THE OPTIC			
Cust	omized switching units are available on re	guest. All data a						701-30-SiC-RS / Revision 06-08-2020 ©2018 A		