	Specification	Syr	mbol	Condition / Comment						HTS 501-120 SiC Unit			
				I_{off} < 80 μ ADC, T_{case} = 70°C							50	kVDC	
	Maximum Isolation Voltage			Between HV switch and control / GND, continuously							± 50	kVDC	
GS	Max. Housing Insulation Voltage		IS I	Between switch and housing surface, 3 minutes						± 60	kVDC		
Š	Maximum Turn-On Peak Current		. ,	T_{case} = t_p < 200 μ s, duty cycle <1%							1200		
RATINGS	No in a Continue Lord Count			25°C t _p < 1 ms, duty cycle <1%							760	ADC	
				t_p < 10 ms, duty cycle <1% t_p < 100 ms, duty cycle <1%							230 120		
MAXIMUM				- -		~170	1 /0			10	ADC		
	Maximum Continuous Load Current		. ,	T _{case} = Standard devices 25°C Devices with option DLC							100	ADC	
Ā	Max. Continuous Power Dissipation			T _{case} = Standard devices & FC, forced air 4 m/s							150	-	
	Linear Derating			25°C Devices with option DLC							4500	Watt	
5				Above Standard devices & FC, forced air 4 m/s							2.85		
70				25°C Devices with option DLC							475	W/K	
ABSOLUTE	Operating Temperature Range		,	Standard devices & options CF, GCF, ILC. (Option DLC)					-4070	C°			
4	Storage Temperature Range		;	Switches with option ILC may require frost protection!						-4090	C°		
	Max. Permissible Magnetic Field			Homogeneous steady-field, surrounding the whole switch					25	mT			
	Max. Auxilliary Voltage			Built-in overvoltage limiter (replaceable)						5.5	VDC		
	Permissible Operating Voltage Range			Unipolar operation (one switch pole grounded or floated)						0 ± 50	kVDC		
	Typical Breakdown Voltage			NOTE: V _{br} is a test parameter for quality control purposes only. Not applicable in						56	kVDC		
	Typical Off-State Current			0.8xV _O , T _{case} =2570°C, reduced l _{off} on request						< 80	µADC		
	Typical Turn-On Resistance			Each switching path			0.1 x I _{P(max)} , T _{case} =25°C				0.22	- P. 12 0	
					ty cycle < 1% $1.0 \times I_{P(max)},$						0.46		
				1.0 x I _{P(max)} , T _{case} =70°C						0.5		Ohm	
	Typical Propagation Delay Time			Resistive load, 0.1 x I _{P(max)} , 0.8 x V _{O(max)} , 50-50%				1			200	ns	
	Typical Output Pulse Jitter	tj			matched inp						3	ns	
	Typical Turn-On Rise Time		1)	Resistive load, 10-90% 0.1 x $V_{O(max)}$, $I_L = 0.1$ x $I_{p(max)}$							20		
				$0.8 \times V_{O(max)}$							50		
S				$0.8 \times V_{O(max)}$, $I_L = 1.0 \times I_{p(max)}$					p(max)	55 ns			
4CTERISTICS	Maximum Turn-On Time		-		o limitation						∞ 	ns	
	Minimum Turn-On Time			ton(min) can be customized. Please consult factor			onsult factory	<u>'</u>		250			
	Maximum Turn-Off Time		. ,	No limitation						∞ 250			
	Minimum Turn-Off Time Max. Continuous Switching			toff(min) can be customized. Please consult factory (② V _{aux} = 5.00 V Standard devices without HFS option					tion		250 <5	ns	
CHARA	Frequency			Sw. shutdown if f _(max) is exceeded Standard devices with HFS supply Opt. HFS + sufficient cooling option							40		
E											80	kHz	
_	Maximum Burst Frequency			Use option HFB for >10 pulses within 20µs or less							500	kHz	
ECTRICAL	Maximum Number of Pulses / Burst			@ f _{b(max)} Standard						> 10 Use option HFB for >10	Pulses		
18	Waximam variber of Falses / Barst			Note: Option HFB requires external buffer capacitors with a voltage Option I-HFB							>100 ose option nr B tot >10	1 01000	
EC				rating of > 630VDC and a cpacitance of 100nF per additional Option HFB							>1000		
EL	Coupling Capacitance			HV side against control side							<100	pF	
	Natural Capacitance			Between switch poles, @ 0.5 x V _{O(max)}						<50	pF		
	<u> </u>			The V _{ctrl} has no impact on the output pulse shape.							3 10	VDC	
	, , , , ,			The +5 V supply is not required in the HFS mode.						5	VDC		
	Typical Auxiliary Supply Current				VDC, T _{case} =			0.01 x f _{(m}	nax)		TBD		
						@ f _(max)				mADC			
	Fault Signal Output			Switch will be turn off, if f>f _(max) , V _{aux} <4.75V or T _{case} >75°C						H=4V, L=0.5V	VDC		
	Ont HES Ext Supply Voltage 1/4			Fault condition is indicated by a logical "L"							15	VDC	
				· · · · · · · · · · · · · · · · · · ·							TBD	VDC	
	Intrinsic Diode Forward Voltage			T _{case} = 25°C, I _F = 0.3 x I _{P(max)}					'		<30	VDC	
	Diode Reverse Recovery Time			$T_{case} = 25^{\circ}C$, $I_F = 0.3 \times I_{P(max)}$, $di/dt = 100 \text{ A/}\mu\text{s}$							<50	ns	
	Dimensions			Standard housing						Please contact the	+		
9				Devices with option CF, non-isolated cooling fins							manufactured!	mm ³	
38			1	Devices with option DLC									
HOUSING	Weight		;	Standard housing							Please contact the		
Ĭ			Devices with option CF, non-isolated cooling fins								manufactured!	g	
			Devices with option DLC										
SN				npatible with Schmitt-Trigger characteristics. Control voltage 2-10 V (3-5 V recommended for low jitter).									
					pin is internally connected with the safety earthing terminal (threaded insert) on bottom side.								
9	Edgic GND / 5V Return 5V Auxiliary Supply Fault Signal Output Inhibit Signal Input LED Indicators Fin 2 / Black. The ground pin is internally connected with the safety earthing terminal (threaded insert) on bottom side. Fin 2 / Black. The ground pin is internally connected with the safety earthing terminal (threaded insert) on bottom side. Fin 3 / Red. The 5 V input is used for rep rates up to the specified max. frequency [max]. Higher rep rates require option HFS. Fin 4 / Orange. TTL output, short circuit proof. Indicating switch & driver over-heat, over-frequency, low auxiliary voltage. L = Fault. Inhibit Signal Input LED Indicators GREEN: "Auxiliary power good, switch OFF". YELLOW: "Control signal received, switch ON". RED: "Fault condition, switch OFF".												
Ş	Inhibit Signal Input Pin 5 / Green. TTL compatible, Schmitt-Trigger characteristics for the connection of extern												
3			er good, switch OFF". YELLOW: "Control signal received, switch ON". RED: "Fault condition, switch OFF"										
									e < 60 s @ $3xPd(max)$, $\Delta T=25K$ (50 to 75°C). Separate driver				
	HTS 501-120 SiC Transistor Switch, 50 kVDC,	Ches Wit Option LP	ith option DLC: 65°C, response time < 3 s @ 3xPd(max), ∆T=25K (40 to 65°C), coolant flow > 3l / min. Separate driver protection. P Low Pass. Input filter for increased noise immunity. Option UL-94 Flame retardant casting resin, UL94-V0										
	Translator Switch, 50 NVDO		Option HF		Frequency Burst			1	Option I-I		Integrated Free-Wheeling Diode. In connection with inductive load only.		
-			Option HF	S High	High Frequency Switching (two auxiliary supply inputs V1 & V2) Option I					FWDN	Integrated Freewheeling Diode Network. In connection with inductive loa	ad.	
WG			Option I-H		Integrated High Frequency Burst Option F Soft Transition Time decrease the rise and fall time by 20% Option S					, ,			
ER			Option S-						Option S Option T		Separated control unit. Control unit with LED indicators in a separate no Tubular Housing	uəli iy.	
ORDERING			Option Mi						Option C		Copper Cooling Fins. P _{d(max)} can be increased by the factor 3 to 1	0.	
0			Option PC	C Puls	Pulser Configuration. Switch combined with custom specific Option C					CF	• • • • • • • • • • • • • • • • • • • •		
		0	Option ISC	0-80 80k\						_C	Indirect Liquid Cooling (for water). P _{d(max)} can be increased by the factor 3 to 15.		
			Option I-P		rated part compo				Option D		Direct Liquid Cooling. $P_{\text{d(max)}}$ can be increased by the factor 10 to 100.		
Cust	omized switching units are available on i	equest. All data	and spec	cifications su	bject to change	without notic	e. Please visit w	ww.behlke.c	com for up	o-dates.	Revision 17-06-2019 ©2017 All rights re	eserved	