	Specification Symbol Condition / Comment								HTS 401-120 SiC Unit						
	Maximum Operating Voltage	V _{O(max)}	I _{off} < 20 μADC, T _{case} = 70°C							40	kVDC				
	Maximum Isolation Voltage	V O(max)		Between HV switch and control / GND, continuously						± 50	kVDC				
S	Max. Housing Insulation Voltage	V _{INS}					,			± 50 ± 70	kVDC				
NG	Maximum Turn-On Peak Current	I _{P(max)}	T _{case} =	Between switch and housing surface, 3 minutes $T_{csse} = t_p < 200 \mu s, duty cycle < 1\%$						1200					
RATINGS		. (11647)	25°C	$t_p < 1 \text{ ms, d}$						760	ADC				
8						uty cycle <1%				230					
M				t _p < 100 ms, duty cycle <1%						120					
MAXIMUM	Maximum Continuous Load Current	I _{L(max)}	T _{case} = Standard devices							10	ADC				
ZA I	Max. Continuous Power Dissipation	P _{d(max)}	25°C Devices with option DLC Tcase = Standard devices & FC, forced air 4 m/s							240 120					
	Wax. Continuous I Gwel Biosipation	1 u(max)	25°C Devices with option DLC							4800	Watt				
4 <i>BSOLUTE</i>	Linear Derating		Above Standard devices & FC, forced air 4 m/s							2.85	VALUE				
350	Operating Temperature Range	To	25°C Devices with option DLC Standard devices & options CF, GCF, ILC. (Option DLC)							475 -4070	W/K C°				
A	Storage Temperature Range	Ts		Switches with option ILC may require frost protection!						-4070 -4090	C°				
	Max. Permissible Magnetic Field	В		Homogeneous steady-field, surrounding the whole switch						25	mT				
	Max. Auxilliary Voltage	V _{aux}		Built-in overvoltage limiter (replaceable)						5.5	VDC				
	Permissible Operating Voltage Range	Vo	Unipolar operation (one switch pole grounded or floated)							0 ± 40	kVDC				
	Typical Breakdown Voltage	V _{br}	NOTE: \/ is a test parameter for quality							44	+				
			ontrol purposes only. Not applicable in loff > 0.5 mA					kVDC							
	Typical Off-State Current	loff								20	μADC				
	Typical Turn-On Resistance	R _{stat}				0.1 x I _{P(max)} , T _{case} =25°C 1.0 x I _{P(max)} , T _{case} =25°C				0.18					
			t _p < 1µs, di	uty cycle < 19	%				0.37		01:				
	Typinal Propagation Dalay Time	4	1.0 x l _{P(max)} ,							0.39	Ohm				
	Typical Propagation Delay Time	t _{d(on)}	Resistive load, 0.1 x I _{P(max)} , 0.8 x V _{O(max)} , 50-50%							200	ns				
	Typical Output Pulse Jitter	t _j	Impedance matched input, V _{aux} / V _{ctrl} = 5.00 VDC								ns				
	Typical Turn-On Rise Time	t _{r(on)}	Resistive lo	Resistive load, 10-90% $0.1 \times V_{O(max)}$, $I_L = 0.1 \times I_{p(max)}$						30 40					
						$0.8 \times V_{O(max)}$, $I_{L} = 0.1 \times I_{p(max)}$ $0.8 \times V_{O(max)}$, $I_{L} = 1.0 \times I_{p(max)}$			60		ns				
SS	Maximum Turn-On Time to		No limitatio					p(max)	8		113				
	Minimum Turn-On Time	t _{on(max)}	No limitation t _{on(min)} can be customized. Please consult factory							200	ns				
RIS	Maximum Turn-Off Time	t _{off(max)}	No limitatio	•						∞ ∞	113				
7	Minimum Turn-Off Time	t _{off(min)}	t _{off(min)} can be customized. Please consult factory							200	ns				
AC	Max. Continuous Switching	f _(max)	@ V _{aux} = 5.				ces without HFS option			3.2	110				
AR	Frequency	I (IIIdX)	_	Sw. shutdown if f _{max} is Standard devices with HFS supply						50					
CH	,	exceeded Opt. HFS + sufficient cooling option					-		80	kHz					
7	Maximum Burst Frequency	f _{b(max)}	Use option HFB for >10 pulses within 20µs or less							500	kHz				
ELECTRICAL	Maximum Number of Pulses / Burst	N _(max)	@ f _{b(max)} Standard							> 10 Use option HFB for >10	Pulses				
3	Westingth Name of Pales	(max)	Note: Option HFB requires external buffer capacitors with a voltage Option I-HFB							>100	1 0.000				
EC			rating of > 630V	DC and a cpacitanc	e of 100nF per	additional	Option H			>1000					
E	Coupling Capacitance	Сс	HV side against control side							<100	pF				
	Natural Capacitance	Between switch poles, @ 0.5 x V _{O(max)}							<50	pF					
	Control Voltage Range	V_{ctrl}	The V _{ctrl} h	The V _{ctrl} has no impact on the output pulse shape.						3 10	VDC				
	Auxiliary Supply Voltage Range	V _{aux}	The +5 V s	supply is not i	required in	the HFS mo	ode.			5	VDC				
	Typical Auxiliary Supply Current laux		$V_{aux} = 5.00$) VDC, T _{case} =	25°C.		0.01 x f _{(r}	nax)		260					
				ent limitation a			@ f _(max)			800	mADC				
	Fault Signal Output		Switch will be turn off, if f>f _(max) , V _{aux} <4.75V or T _{case} >75°C							H=4V, L=0.5V	1117 (DC				
	1		Fault condition is indicated by a logical "L" Stability ±3%, current consumption <0.4 mA/kHz @ 25°C							11 44, 2 0.04	VDC				
	0.4.1150.5.4.2	.,			ted by a lo	ogical "L"				,	VDC				
	Opt. HFS, Ext. Supply Voltage V1	V _{HFS(V1)}	Stability ±3	3%, current c	ted by a lo	ogical "L" on <0.4 mA/k	Hz @ 25°C			15	VDC VDC				
	Opt. HFS, Ext. Supply Voltage V2	V _{HFS(V2)}	Stability ±3 Stability ±3	3%, current c 3%, current c	ted by a loonsumption	ogical "L" on <0.4 mA/k	Hz @ 25°C			15 270	VDC VDC VDC				
	Opt. HFS, Ext. Supply Voltage V2 Intrinsic Diode Forward Voltage	V _{HFS(V2)}	Stability ±3 Stability ±3 T _{case} = 25°	3%, current c 3%, current c C, $I_F = 0.3 x$	onsumptionsump	ogical "L" on <0.4 mA/k on <0.9 mA/k	Hz @ 25°C Hz @ 25°C			15 270 <30	VDC VDC VDC VDC				
	Opt. HFS, Ext. Supply Voltage V2 Intrinsic Diode Forward Voltage Diode Reverse Recovery Time	V _{HFS(V2)} V _F t _{rrc}	Stability ±3 Stability ±3 T _{case} = 25° T _{case} = 25°	3%, current c 3%, current c C, $I_F = 0.3 x$ C, $I_F = 0.3 x$	onsumptionsump	ogical "L" on <0.4 mA/k on <0.9 mA/k	Hz @ 25°C Hz @ 25°C			15 270 <30 <50	VDC VDC VDC				
4	Opt. HFS, Ext. Supply Voltage V2 Intrinsic Diode Forward Voltage	V _{HFS(V2)}	Stability ± 3 Stability ± 3 $T_{case} = 25^{\circ}$ $T_{case} = 25^{\circ}$ Standard h	3%, current c 3%, current c C, $I_F = 0.3 x$ C, $I_F = 0.3 x$ nousing	ted by a loonsumption on sumption on sumption lemax lemax, di/d	ogical "L" on <0.4 mA/k on <0.9 mA/k It = 100 A/µs	Hz @ 25°C Hz @ 25°C			15 270 <30 <50	VDC VDC VDC VDC ns				
ING	Opt. HFS, Ext. Supply Voltage V2 Intrinsic Diode Forward Voltage Diode Reverse Recovery Time	V _{HFS(V2)} V _F t _{rrc}	Stability ±3 Stability ±3 T _{case} = 25° T _{case} = 25° Standard h Devices wi	3%, current c $3%$, current c $3%$, current c $3%$, $1%$, $1%$ $1%$ $1%$ $1%$ $1%$ $1%$ $1%$ $1%$	nsumptionsumpt	ogical "L" on <0.4 mA/k on <0.9 mA/k It = 100 A/µs	Hz @ 25°C Hz @ 25°C			15 270 <30 <50	VDC VDC VDC VDC				
USING	Opt. HFS, Ext. Supply Voltage V2 Intrinsic Diode Forward Voltage Diode Reverse Recovery Time Dimensions	V _{HFS(V2)} V _F t _{rrc}	Stability ±3 Stability ±3 T _{case} = 25° T _{case} = 25° Standard h Devices wi Devices wi	3%, current c 3%, current c C, $I_F = 0.3 x$ C, $I_F = 0.3 x$ nousing ith option CF, ith option DL(nsumptionsumpt	ogical "L" on <0.4 mA/k on <0.9 mA/k It = 100 A/µs	Hz @ 25°C Hz @ 25°C			15 270 <30 <50 Please contact the manufactured!	VDC VDC VDC VDC ns				
HOUSING	Opt. HFS, Ext. Supply Voltage V2 Intrinsic Diode Forward Voltage Diode Reverse Recovery Time	V _{HFS(V2)} V _F t _{rrc}	Stability ±3 Stability ±3 T _{case} = 25° T _{case} = 25° Standard h Devices wi Devices wi Standard h	3% , current c 3% , current c C , $I_F = 0.3 \text{ x}$ C , $I_F = 0.3 \text{ x}$ nousing ith option CF, ith option DL0 nousing	ted by a loonsumpticonsumpticonsumpticonsumpticolP(max) IP(max), di/donon-isolaC	ogical "L" on <0.4 mA/k on <0.9 mA/k it = 100 A/µs ated cooling t	Hz @ 25°C Hz @ 25°C			15 270 <30 <50 Please contact the manufactured!	VDC VDC VDC VDC ns mm³				
HOUSING	Opt. HFS, Ext. Supply Voltage V2 Intrinsic Diode Forward Voltage Diode Reverse Recovery Time Dimensions	V _{HFS(V2)} V _F t _{rrc}	Stability ±3 Stability ±3 T _{case} = 25° T _{case} = 25° Standard h Devices wi Devices wi Standard h Devices wi	3%, current c $3%$, current c $1%$, $1%$	ted by a loonsumpticonsumpticonsumpticonsumpticonsumpticole (IP(max), IP(max), di/doministration, non-isola C	ogical "L" on <0.4 mA/k on <0.9 mA/k it = 100 A/µs ated cooling t	Hz @ 25°C Hz @ 25°C			15 270 <30 <50 Please contact the manufactured!	VDC VDC VDC VDC ns				
	Opt. HFS, Ext. Supply Voltage V2 Intrinsic Diode Forward Voltage Diode Reverse Recovery Time Dimensions Weight Control Signal Input Pin 1 / Yellow. T	V _{HFS(V2)} V _F t _{rrc} LxWxH	Stability ±3 Stability ±3 T _{case} = 25° T _{case} = 25° Standard h Devices wi Devices wi Standard h Devices wi Devices wi Devices wi	3% , current c 3% , current c C , $I_F = 0.3 \text{ x}$ C , $I_F = 0.3 \text{ x}$ nousing ith option CF, ith option DL0 nousing	ted by a lo onsumptic onsumptic l _{P(max)} d _{P(max)} , di/d , non-isola C	ogical "L" on <0.4 mA/k on <0.9 mA/k it = 100 A/µs ated cooling the	Hz @ 25°C Hz @ 25°C ins		jitter).	15 270 <30 <50 Please contact the manufactured!	VDC VDC VDC VDC ns mm³				
	Opt. HFS, Ext. Supply Voltage V2 Intrinsic Diode Forward Voltage Diode Reverse Recovery Time Dimensions Weight Control Signal Input Logic GND / 5V Return Pin 1 / Yellow. T Pin 2 / Black. Th	V _{HFS(V2)} V _F t _{rrc} LxWxH	Stability ±3 Stability ±3 T _{case} = 25° T _{case} = 25° Standard h Devices wi Devices wi Standard h Devices wi with Schmitt-Tr s internally conn	3%, current c 3%, current c C , $I_F = 0.3 x$ C , $I_F = 0.3 x$ nousing ith option CF, ith option DL nousing ith option DL ith option DL ith option DL ith option DL ith option DL ith option DL	ted by a lo onsumption on sumption on sumption on sumption le (max) le (max), di/d on on-isola consistent of consistent on one sum of the sum o	ogical "L" on <0.4 mA/k on <0.9 mA/k it = 100 A/µs ated cooling the cooling th	Hz @ 25°C Hz @ 25°C ins ins 5 V recommend dinsert) on bot	ded for low tom side.		15 270 <30 <50 Please contact the manufactured!	VDC VDC VDC VDC ns mm³				
	Opt. HFS, Ext. Supply Voltage V2 Intrinsic Diode Forward Voltage Diode Reverse Recovery Time Dimensions Weight Control Signal Input Logic GND / 5V Return SV Auxiliary Supply Fault Signal Output Pin 3 / Red. The Pin 3 / Red. The Pin 4 / Orange. 1	VHFS(V2) VF trrc LxWxH TL compatible e ground pin i SV input is u TL output, sh	Stability ±3 Stability ±3 T _{case} = 25° T _{case} = 25° Standard h Devices wi Devices wi Standard h Devices wi Devices wi with Schmitt-Tr s internally conn sed for rep rates ort circuit proof.	3%, current c 3%, current c C, I _F = 0.3 x C, I _F = 0.3 x nousing ith option CF, ith option DL ith option DL ity	ted by a lo consumption on sumption of sumption on sumption of sumption of sumption of sumption on sumption of sum	ogical "L" on <0.4 mA/k on <0.9 mA/k it = 100 A/µs ated cooling if ated cooling if roltage 2-10 V (3 terminal (threadeency f _{(max}), High -heat, over-freqt	Hz @ 25°C Hz @ 25°C ins ins 5 V recomment dinsert) on bot en rates requency, low auxilieroy, l	ded for low tom side. uire option h ary voltage.	HFS. . L = Faul	15 270 <30 <50 Please contact the manufactured! Please contact the manufactured!	VDC VDC VDC VDC ns mm³				
	Opt. HFS, Ext. Supply Voltage V2 Intrinsic Diode Forward Voltage Diode Reverse Recovery Time Dimensions Weight Control Signal Input Logic GND / 5V Return 5V Auxiliary Supply Fault Signal Output Inhibit Signal Input Pin 3 / Red. The Pin 4 / Orange. T Pin 5 / Green. T	VHFS(V2) VF trrc LxWxH TL compatible e ground pin i 5 V input is u	Stability ±3 Stability ±3 T _{case} = 25° T _{case} = 25° Standard h Devices wi Devices wi Devices wi Devices wi with Schmitt-Tr s internally connect for rep rates ord circuit proof. Schmitt-Trigger	3% , current c 3% , current c C , $I_F = 0.3 x$ C , $I_F = 0.3 x$ nousing ith option CF, ith option DLG induction of the option DLG induction of the option DLG induction of the specific ledted with the sate up to the specific ladicating switch or characteristics for	ted by a lo consumption on sumption on sumption on sumption le (max). In (ma	ogical "L" on <0.4 mA/k on <0.9 mA/k it = 100 A/µs ated cooling the cooling th	Hz @ 25°C Hz @ 25°C ins ins 5 V recomment dinsert) on boter rep rates requency, low auxiliafety circuits. L	ded for low tom side. irre option h arry voltage. = Switch Ir	HFS. . L = Faul nhibited.	15 270 <30 <50 Please contact the manufactured! Please contact the manufactured!	VDC VDC VDC VDC ns mm³				
FUNCTION HOUSING	Opt. HFS, Ext. Supply Voltage V2 Intrinsic Diode Forward Voltage Diode Reverse Recovery Time Dimensions Weight Control Signal Input Logic GND / 5V Return SV Auxiliary Supply Fault Signal Output Inhibit Signal Input LED Indicators Temperature Protection A) Standard Switch	VHFS(V2) VF trrc LXWXH TL compatible e ground pin i 5 V input is u TL output, sh	Stability ±3 Stability ±3 T _{case} = 25° T _{case} = 25° Standard h Devices wi Devices wi Standard h Devices wi bevices wi with Schmitt-Tr is internally conn sed for rep rates ort circuit proof, Schmitt-Trigger is, switch OFF".	3%, current c 3%, current c C, I _F = 0.3 x C, I _F = 0.3 x nousing ith option CF, ith option DL ity	ted by a lo consumption on sumption of sumption on sumption of sumption on sumption of sumption on sumption on sumption on sumption on sumption on sum	ogical "L" on <0.4 mA/k on <0.9 mA/k it = 100 A/µs ated cooling the standard cooling the stan	Hz @ 25°C Hz @ 25°C ins ins ins 5 V recomment of user) on bother reprates requency, low auxiliard, low auxiliard, recursits. L. RED: "Fault	ded for low tom side. uire option h ary voltage. = Switch Ir condition, s	HFS. L = Faul nhibited. switch OF	15 270 <30 <50 Please contact the manufactured! Please contact the manufactured!	VDC VDC VDC VDC ns mm³				
	Opt. HFS, Ext. Supply Voltage V2 Intrinsic Diode Forward Voltage Diode Reverse Recovery Time Dimensions Weight Control Signal Input Logic GND / 5V Return 5V Auxiliary Supply Fault Signal Output Inhibit Signal Output LED Indicators Temperature Protection Referent Syandard switc Jasa	VHFS(V2) VF trrc LxWxH TL compatible e ground pin i 5 V input is u TL compatible ry power good with the sand switch ΔT=25K (40 to 25 to 2	Stability ±3 Stability ±3 Tcase = 25° Tcase = 25° Standard h Devices wi Devices wi Standard h Devices wi Minumer of the control of the control Schmitt-Trigger, switch OFF*. Sew with option C 65°C), codent fix	3% , current c 3% , current c C , $I_F = 0.3 \text{x}$ C , C	ted by a lo consumption on sumption on sumption on sumption le (max). di/d non-isola C c consumption of the	ogical "L" on <0.4 mA/k on <0.9 mA/k it = 100 A/µs ated cooling it ated cooling it terminal (threade ency f _(max) , Higher eived, switch ON response time < 6 tection.	Hz @ 25°C Hz @ 25°C ins ins ins 5 V recomment d insert) on bot er rep rates requ ency, low auxili afety circuits. L ". RED: "Fault 0 s @ 3xPd(ma)	ded for low tom side. irie option h any voltage. = Switch Ir condition, s, \(\lambda\) AT=25K	HFS. L = Faul nhibited. switch OF (50 to 75°	15 270 <30 <50 Please contact the manufactured! Please contact the manufactured! Please contact the manufactured!	VDC VDC VDC VDC ns mm³				
	Opt. HFS, Ext. Supply Voltage V2 Intrinsic Diode Forward Voltage Diode Reverse Recovery Time Dimensions Weight Control Signal Input Logic GND / 5V Return SV Auxiliary Supply Fault Signal Output Inhibit Signal Input LED Indicators Temperature Protection A) Standard Switch	VHFS(V2) VF trrc LXWXH TL compatible e ground pin i 5 V input is u TL output, sh	Stability ±3 Stability ±3 Tcase = 25° Tcase = 25° Standard h Devices wi Devices wi Devices wi Mith Schmitt-Tr S internally conn sed for rep rates ort circuit proof. Schmitt-Trigger, switch OFF". nes with option C LP Low	3%, current c 3%, current c C, I _F = 0.3 x C, I _F = 0.3 x nousing ith option CF, ith option DL ity	ted by a lo consumption on sumption on sumption on sumption le (max). Ile (max). Ide (ma	ogical "L" on <0.4 mA/k on <0.9 mA/k it = 100 A/µs ated cooling the stated cooling the st	Hz @ 25°C Hz @ 25°C ins ins 5 V recommend dinsert) on bote ar ep rates requency, low auxiliafety circuits. L ". RED: "Fault o s @ 3xPd(max	ded for low tom side. uire option h ary voltage. = Switch Ir condition, s	HFS. L = Faul nhibited. switch OF (50 to 75°	15 270 <30 <50 Please contact the manufactured! Please contact the manufactured!	VDC VDC VDC VDC ns mm³				
FUNCTION	Opt. HFS, Ext. Supply Voltage V2 Intrinsic Diode Forward Voltage Diode Reverse Recovery Time Dimensions Weight Control Signal Input Logic GND / 5V Return 5V Auxiliary Supply Fault Signal Output Inhibit Signal Output LED Indicators Temperature Protection Referent Syandard switc Jasa	VHFS(V2) VF trrc LxWxH TL compatible e ground pin i 5 V input is u TL compatible to provide the compatible of the compatible of the compatible of the sand switch and switch the compatible of	Stability ±3 Stability ±3 T _{case} = 25° T _{case} = 25° Standard h Devices wi Devices wi Devices wi Devices wi Standard h Devices wi of crep rates of circuit proof. Schmitt-Trigge i, switch OFF". es with option C 66°C), colant fice Devices Low HFB High	3% , current c 3% , current c C , $I_F = 0.3 \text{ x}$ C , $I_F = 0.3 \text{ x}$ C , $I_F = 0.3 \text{ x}$ nousing ith option DL0 in the option	ted by a lo consumptic onsumptic ons	ogical "L" on <0.4 mA/k on <0.9 mA/k it = 100 A/µs ated cooling f ated cooling f roltage 2-10 V (3 terminal (threade tency f(max), High -heat, over-freq to external s eived, switch ON response time < 6 tection.	Hz @ 25°C Hz @ 25°C ins ins ins 5 V recomment d insert) on bother rep rates requency, low auxili afety circuits. L ". RED: "Fault o s @ 3xPd(max) nal	ded for low tom side. jire option h ary voltage. = Switch Ir condition, s, k), \(\Delta T=25K\) \(\text{ Option UI}\) \(\text{ Option I-I}\)	HFS. L = Faul nhibited. switch OF (50 to 75° IL-94 FWD)	15 270 <30 <50 Please contact the manufactured! Please contact the manufactured! Please contact the manufactured! It. F" "C). Separate driver protection. B) Switches with option DLC: 65°C, responsible to the protection of t	VDC VDC VDC VDC Ns mm³ g				
FUNCTION	Opt. HFS, Ext. Supply Voltage V2 Intrinsic Diode Forward Voltage Diode Reverse Recovery Time Dimensions Weight Control Signal Input Logic GND / 5V Return 5V Auxiliary Supply Fault Signal Output Inhibit Signal Output LED Indicators Temperature Protection Referent Syandard switc Jasa	VHFS(V2) VF trrc LXWXH TL compatible e ground pin i 5V input is u TL output, sh L compatible y power good-hes and switch \(\Delta \) T=25K (40 to \(\Delta \) Option Option Option Option	Stability ±3 Stability ±3 T _{case} = 25° T _{case} = 25° Standard h Devices wi Devices wi Standard h Devices wi Standard h Devices wi with Schmitt-Tr is internally conn sed for rep rates ort circuit proof. Schmitt-Trigger i, switch OFF". hes with option C 66°C), coolant fit. LP Low HFB High HFS Integ	3%, current c 3%, current c C, I _F = 0.3 x C, I _F = 0.3 x nousing ith option CF, ith option DLG igger characterist nected with the sa to up to the specific Indicating switch or characteristics for YELLOW. "Cont CF, GCF: Thermot ow > 31 / min. Sepa Pass. Input filter n Frequency Burst n Frequency Burst or Frequency Switt grated High Fr	ted by a la consumptic onsumptic ons	ogical "L" on <0.4 mA/k on <0.9 mA/k it = 100 A/µs ated cooling the standard cooling the stan	Hz @ 25°C Hz @ 25°C Hz @ 25°C ins ins 5 V recommend dinsert) on bot er rep rates requency, low auxiliafety circuits. L ". RED: "Fault 0 s @ 3xPd(max) nal uts V1 & V2)	ded for low tom side. Irie option I- ary voltage. = Switch Ir condition, s. (), \(\Delta T=25K\) Option II- Option II- Option P	HFS. L = Faul nhibited. switch OF (50 to 75° L-94 FWD FWDN T-C	15 270 <30 <50 Please contact the manufactured! Please contact the manufactured! Please contact the manufactured! It. F" C). Separate driver protection. B) Switches with option DLC: 65°C, responsible to the protection of th	VDC VDC VDC VDC Ns mm³ g				
FUNCTION	Opt. HFS, Ext. Supply Voltage V2 Intrinsic Diode Forward Voltage Diode Reverse Recovery Time Dimensions Weight Control Signal Input Logic GND / 5V Return 5V Auxiliary Supply Fault Signal Output Inhibit Signal Output LED Indicators Temperature Protection Referent Syandard switc Jasa	VHFS(V2) VF trrc LXWXH TL compatible e ground pin i 5 V input is u TL output, shr L compatible y power good hes and switch \(\text{\text{\$A\$}} \) TL compatible of \(\text{\text{\$A\$}} \) The \(\text{\text{\$A\$}} \) Option \(\text{\text{\$O\$}} \)	Stability ±3 Stability ±3 Tcase = 25° Tcase = 25° Standard h Devices wi Devices wi Standard h Devices wi Standard h Devices wi With Schmitt-Tris internally conn sed for rep rates of circuit prosofo, Schmitt-Trigger 1, switch OFF°. 1, swit	3%, current c 3%, current c C, I _F = 0.3 x C, I _F = 0.3 x nousing ith option CF, ith option DL(nousing ith option DL(igger characterist ected with the sa up to the specific rectal with the sa rectal substitution of specific rectal substitut	ted by a la consumption on sumption on sum	ogical "L" on <0.4 mA/k on <0.9 mA/k it = 100 A/µs ated cooling fi ated cooling fi aterd co	Hz @ 25°C Hz @ 25°C Hz @ 25°C Tins Tins Tins Solution of the properties requered, low auxiliafety circuits. L The RED: "Fault of the properties of the	ded for low tom side. Jirie option I- ary voltage. Switch Ir condition, s. (), $\Delta T = 25K$ Option I-I Option I-I Option P	HFS. L = Faul nhibited. switch OF (50 to 75° L-94 FWD FWDN T-C EP-C	15 270 <30 <50 Please contact the manufactured! Please contact the manufactured! Please contact the manufactured! It. FF" *C). Separate driver protection. B) Switches with option DLC: 65°C, responsible to the protection of	VDC VDC VDC VDC Ns mm³ g				
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