	Specification	ication Symbol Condition / Comment					HTS 201-40 LC2 HTS 241-40 LC2 HTS 301-40 LC4 Unit			
	Maximum Operating Voltage				20	24	30	kVDC		
	Minimum Operating Voltage	V _{O(min)}	$t_{\text{r(on)}}$ and $t_{\text{r(off)}}$ may increase sligthly if operated below 5% of $V_{\text{O(max)}}$				0		kVDC	
	Typical Breakdown Voltage	V _{br}	NOTE: V_{br} is a test parameter for quality control purposes only. Not applicable in normal operation! $I_{off} > 0.5 \text{ mA}$		22	26	33	kVDC		
	· ·		ontrol / GND, continuously		± 40	± 40	± 40	VDC		
	Max. Housing Insulation Voltage	ge V _{INS}	Between switch and hous			± 40	± 40	± 40	VDC	
	Maximum Turn-On Peak Curre	ent I _{P(max)}	T _{case} = 25°C		duty cycle <1%		400		ADC	
					ıty cycle <1%		236			
					duty cycle <1%		114			
					duty cycle <1%		8.7			
	Maximum Continuous Load Curr		T _{case} = 25°C		(max) on request		3.4		ADC	
	Max. Continuous Power Dissipat	tion P _{d(max)}	T _{case} = 25°C		evices & FH, forced air 4 m/s	25	30	37.5	Watt	
	Linear Derating		Above 25°C		evices & FH, forced air 4 m/s	0.02	0.025	0.03	W/K	
	Operating Temperature Range		Standard devices & option				-4070 (60)		°C	
	Storage Temperature Range Ts		Switches with option ILC may require frost protection!			-4090			°C	
	Max. Permissible Magnetic Field B		Homogeneous steady-field, surrounding the whole switch				25		mT	
3	Max. auxiliary Voltage Vo				0 ± 20	0 ± 24	0 ± 30	kVDC		
•	Max. Auxilliary Voltage	V _{aux}	Built-in overvoltage limiter (replaceable)			5.25		VDC		
	Max. Off-State Current	I _{off}	0.8xV ₀ , T _{case} =2570°C, reduced l _{off} on request			< 40		μADC		
5	Static On-Resistance	R _{stat}	$T_{case} = 25^{\circ}C$ $0.1x p_{(max)} $			1.9	2.3	2.9		
				Ip _(max)		4.5	5.5	2.9	Ohm	
MAAIMOIM	Galvanic isolation	Vi	Continuously		housing/PCB attachment	25	25	40	kVDC	
3				Option PT-HV, pigtails for HV		40	40	40		
				Option P	T-HV + Option ISO-80 1)	80	80	80		
	T ::		Desire 1 1011	0.0.17	50 500/		222		ļ	
5	Typical Propagation Delay Tim		Resistive load, 0.1 x I _{P(max}				200		ns	
2	Typical Turn-On Rise Time	t _{r(on)}	Resistive load, 10-80%	$\begin{array}{l} 0.1 \ x \ V_{O(max)}, \ I_L = 0.1 \ x \ I_{p(max)} \\ 0.5 \ x \ V_{O(max)}, \ I_L = 0.1 \ x \ I_{p(max)} \end{array}$		53	50	46		
ABSOLUTE						58	59 77	83		
₹					l_{max} , $l_{L} = 0.1 \times l_{p(max)}$	68	77	88		
ŀ	Minimum On Time		$0.8 \times V_{O(max)}$, $I_L = 1.0 \times I_{p(max)}$			94	104 150	105	ns	
	Minimum On-Time ton(min		Lower t _{on(min)} on request				150			
-	Maximum On Time		Places note D limitational						ns	
ŀ	Maximum On-Time ton(max)		Please note P _{d(max)} limitations!				∞ 4			
ŀ	Typical Output Pulse Jitter t _j		Impedance matched input, V _{aux} / V _{ctrl} = 5.00 VDC			0.4	1 7	0.4	ns	
-	Max. Switching Frequency $f_{(max)}$ Maximum Burst Frequency $f_{b(max)}$		Please note P _{d(max)} limitations!			0.1	1.7	2.1	kHz	
	1 , -()		Use HFB for >5 pulses within 100 µs				500		kHz	
	Coupling Capacitance Cc			Standard devices & options CF, DLC			<15			
ļ			control side Devices with options GCF, ILC		20	70 150		pF		
-	Natural Capacitance C _N		Capacitance between switch poles at Vo _{max}			68	53	36	pF	
ŀ	Control Voltage Range V _{ctrl} Auxiliary Supply Voltage Range V _{aux}		The V _{ctrl} has no impact on the output pulse shape.				3 10 4.75 5.25		VDC VDC	
ŀ			The +5 V supply is not required in the HFS mode.			200		200	VDC	
	Typical Auxiliary Supply Curre	nt I _{aux}	$V_{aux} = 5.00 \text{ VDC}, T_{case} = 25^{\circ}\text{C}.$ 0.01 x $f_{(max)}$			280 800	230 800	200 800		
ŀ	Fault Signal Output Voltage		Short circuit proof, source/ sink current max. 10mA Ready = High Fault = Low		0 1 (1)	000	> 4.0	000		
	rault Signal Output voltage				, ,		< 0.8		VDC	
ŀ	Control Signal Voltage	V _{tr}			Tault - LOW	+	2-6		VDC	
	Opt. HFS Ext. Supply Voltage		>3VDC recommended			15		VDC		
			Stability ±3% current consumption <0.4 mA/kHz@25°C		190					
	Opt. HFS Ext. Supply Voltage	V2 V _{HFS(V2)}		Stability ±3% current consumption <0.9 mA/kHz@25°C			236	206	VDC	
	Dimensions	LxWxH	Standard housing with option PT-HV			200x150x56	252x175x56	275x150x58		
5			Devices with option FC, Flat case			Please contact the	Please contact the	Please contact the		
211			Devices with option CF, non-isolated cooling fins, standard size			manufactured!	manufactured!	manufactured!	mm ³	
HOUSING	Weight	· · ·	Standard housing, with option PT-HV				Please contact the			
E			Devices with option FC, Flat case Devices with option CF, non-isolated cooling fins, standard size				manufactured!			
	<u> </u>		· ·		•	<u> </u>			g	
	Control Signal Input Pin 1 / Yellow (LS-C: Pin 1). TTL compatible (LS-C: With 100Ω termination). Schmitt-Tr						-		jitter).	
	Logic GND / 5V Return Pin 2 / Black (LS-C: Pin 2). The ground pin is internally connected with the safety eart									
	5V Auxiliary Supply Pin 3 / Red (LS-C: Pin 3). The 5 V input is used for rep rates up to the specified max. I				equency f _{(max).} Hig	her rep rates require of	ption HFS.			
3	Fault Signal Output Pin 4 / Orange (LS-C: Pin 4). TTL output, short circuit proof. Indicating switch & driver				ver-heat, over-fre	quency, low auxiliary	voltage. L = Fault.			
LONCINONS	Logic GND / 5V Return Pin 5 / Black (LS-C: Shielding). The ground pin is internally connected with the safety				arthings terminals	(threaded inserts) on	bottom side.			
5	LED Indicators GR	EEN: "Auxiliary	power good, switch OFF".	YELLOW:	'Control signal received, swite	ch ON". RED: "F	ault condition, switch	OFF"		
	Temperature Protection A) Standard switches and switches with opt. FC, CF, GCF: Thermo to			Thermo trigger 75°C, response	e time < 60 s @ 3x	xPd(max), ∆T=25K (50	to 75°C). Separate	driver		
	protection. B) Switches with option DLC: 65°C, response time < 3 s @ 3xPd(max),				< 3 s @ 3xPd(max), ∆T=25K (4	40 to 65°C), coolan	t flow > 3I / min. Separa	te driver protection.		
	HTS 201.40 LC2 Solid-state Switch, 20kV, 40 A Option LP Low Pass. Input filter			ut filter for increas	sed noise immunity.	Option I-PC Integrated part components according to customer specification.				
					Option UL-94	Flame retardant casting resi		San Inc. 1		
	HTS 301-40 LC2 Solid-state Switch, 30kV, 40 A		Option HFS High Frequency Switching (two auxiliary supply inputs V1 & V2) Option I-HFS Integrated High Frequency Burst			Option I-FWDN	Integrated Free-Wheeling Diod			
			Option I-HFS Integrated High Frequency Burst Option S-TT Soft Transition Time decrease the rise and fall time by 20%		Option I-FWDN Option PT-C	Integrated Freewheeling Diode Pigtail for control connection: F				
2			Option S-TT Soft Transition Time decrease the rise and fall time by 20% Option Min-On Individually increased "Min. On-Time" to avoid unwanted triggering			Option SEP-C	Separated control unit. Control			
			Option Min-Off Individually inc		Time" to avoid unwanted triggering	Option TH	Tubular Housing			
DENING			Option PCC Pulser Configuration. Switch combined with custom specific parts.		Option CF	Copper Cooling Fins. Pd(max	can be increased by the	factor 3 to		
OKDERING										
OKDEKING			Option ISO-40 40kV Isolation	. Isolation Voltage	e increased to 40kV.	Option GCF	Grounded Cooling Flange. Pd(max) can be increased by the	factor 3 to	
OKDEKING			Option ISO-40 40kV Isolation Option ISO-80 80kV Isolation	. Isolation Voltage				max) can be increased by the er). P _{d(max)} can be increased	e factor 3 to d by the factor	
ONDERING			Option ISO-40 40kV Isolation Option ISO-80 80kV Isolation	. Isolation Voltage	e increased to 40kV.	Option GCF Option ILC	Grounded Cooling Flange. Pd(Indirect Liquid Cooling (for wat	max) can be increased by the er). P _{d(max)} can be increased	factor (