| | Specification | Symbol | Condition / Comment | | | HTS 160-200 SCR | Unit |
|-----------------|--|---|---|--|--|---|---------|
| | Maximum Operating Voltage | $V_{O(max)}$ | I _{off} < 50 μADC, T _{case} = 70°C | | | 16 | kVDC |
| RATINGS | Maximum Isolation Voltage | VI | Between HV switch and control / GND, continuously | | | ± 20 | kVDC |
| | Max. Housing Insulation Voltage | V _{INS} | Between switch and housing | | es | ± 20 | kVDC |
| | Maximum Turn-On Peak Current | $I_{P(max)}$ | | t_p < 500 μs , duty cycle <1% | | 2000 | |
| | | | | | | 1000 | ADC |
| 11 | | | t _p < 1 ms, duty cycle <1% | | | 640 | |
| RA | | ļ | t _p < 10 ms, duty cycle <1% | | | 400 | 150 |
| MAXIMUM | Max. Non-Repetitive Peak Current | I _{p(nr)} | Half sine single pulse, tp<200 μs | | | 4000 | ADC |
| | Max. Coutinuous Load Current | - | T _{case} = 25 °C Half sine single pulse, tp<20 μs Standard Plastic case With Option GCF, cooling flange | | | 8000 0.72 | ADC |
| XII | iviax. Codundous Load Current | IL. | | | | 52 | ADC |
| MA | Max. Rate-of-Rise of OFF-State Voltage | dv/dt | | | | 80 | kV/ µs |
| H | max. rate of race of or a cate voltage | av/at | V _{O(max)} , exponential waveform | | | | ικν, μο |
| 7 | Max. Continuous Power Dissipation | $P_{d(max)}$ | T _{Case} = 25°C Standard devices & FC, forced air 4 m/s | | | 14 | Watt |
| ABSOLUTE | | | With opt. GCF, grounding cooling flange | | | 800 | |
| 183 | Linear Derating | | Standard devices & FC, forced air 4 m/s | | | 0.30 | W/K |
| | Occupies Temperatus Descri | - | With opt. GCF, grounding cooling flange Standard devices & options CF, GCF, ILC. (Option DLC) | | | 20 | C° |
| | Operating Temperature Range Storage Temperature Range | T _o | Switches with option ILC may require frost protection! | | | -4070 | C° |
| | Max. Permissible Magnetic Field | B | Homogeneous steady-field, surrounding the whole switch | | | -5090 25 | mT |
| | Max. Auxilliary Voltage | V _{aux} | Built-in overvoltage limiter (replaceable) | | | 5 | VDC |
| | Permissible Operating Voltage Range | Vaux | Builtin overvoilage infilier (replaceable) | | | 0 ± 16 | kVDC |
| | Typical Breakdown Voltage | V _{br} | NOTE: V _{br} is a test parameter for quality | | L > 0 F ^ | 17.6 | kVDC |
| | | | control purposes only. Not applicable in Ioff > 0.5 mA | | | | |
| | Typical Off-State Current | l _{off} | 0.8xV _O , T _{case} =2570°C, reduced I _{off} on request Tcase/flange=25°C | | | < 100 | μADC |
| | Typical Holding Current | | | | | 50 | mADC |
| | Typical On-State Voltage | V _{sat} | Each switching path | Tcase/flang | | 35 15 | |
| | Typical On-State Voltage | | t _p < 1µs, duty cycle < 1% | 0.001 x IP(ma | | 18 | |
| | | | φ • 1μ3, duty cycle • 170 | 0.01 x I _{P(max} | | 28 | VDC |
| | | | | 1.0x I _{P(max)} | , | 80 | 1.20 |
| | Typical Propagation Delay Time t _{d(on)} | | Resistive load, 0.1 x I _{P(max)} , 0. | 8 x V _{O(max)} , 50-50 | 1% | < 200 | ns |
| S | Typical Output Pulse Jitter t _j | | Impedance matched input, V | | | 100 | ps |
| Ĭ | Typical Turn-On Rise Time | t _{r(on)} | Resistive load, 10-90% | | $I_{L} = 0.1 \times I_{p(max)}$ | 300 | |
| RIS | | | | | $I_{L} = 0.1 \times I_{p(max)}$ | 70 | |
| TE | | t _{off,} t _q | $0.8 \times V_{O(max)}$, $I_L = 1.0 \times I_{p(max)}$ | | | 200 | ns |
| CHARACTERISTICS | Typical Turn-Off Time | | Resistive load, 10-90% $0.1 \times V_{O(max)}$, $I_L = 0.1 \times I_{p(max)}$ | | | 35 90 | μs |
| IAR | Maximum On Time | $0.8 \text{ x V}_{O(\text{max})}, \text{ I}_{L} = 1.0 \text{ x I}_{p(\text{max})}$ Please note Pd max. limitation | | | Depending on holding current flow only | no | |
| CF | Maximum On Time ton(max) Please note Pd max. limitation Internal Driver Recovery Time trc | | | | 500 | ns ns | |
| 44 | Max. Continuous Switching f _(max) | | @ Vaux= 5.00 V Standard devices without HFS option | | | 1.3 | 110 |
| TRICAL | Frequency | ·(max) | | dard devices with | | Please contact the | |
| | | | exceeded Opt. HFS + sufficient cooling option | | | manufactured! | kHz |
| ELEC | Maximum Burst Frequency f _{b(max)} | | Use option HFB for >10 pulses within 20µs or less | | | 10 | kHz |
| | Maximum Number of Pulses / Burst N _{(max} | | @ f _{b(max)} Standard | | | 100 Use option HFB for >100 | Pulses |
| | | | Note: Option HFB requires external buffer capacitors with a voltage Option I-HFB | | ' | Please contact the | |
| | | | | 0VDC and a cpacitance of 100nF per additional Option HFB | | manufactured! | |
| | Coupling Capacitance | Cc | HV side against control side | | 10 | pF | |
| | Control Voltage Range V _{ctrl} | | The V _{ctrl} has no impact on the output pulse shape. | | | 4 5 | VDC |
| | Auxiliary Supply Voltage Range | V _{aux} | The +5 V supply is not required in the HFS mode. | | 5 | VDC | |
| | Typical Auxiliary Supply Current I _{aux} | | $V_{\text{aux}} = 5.00 \text{ VDC}, T_{\text{case}} = 25^{\circ}\text{C}.$ Active current limitation above 1A. $0.01 \text{ x f}_{(\text{max})}$ $f_{(\text{max})}$ | | | 200 500 | mADC |
| | Fault Signal Output | | Active current limitation above 1A. @ f _(max) Switch will be turn off, if f>f _(max) , V _{aux} <4.75V or T _{case} >75°C | | | H=4V, L=0.5V | VDC |
| | Taut Oighai Oatpat | Fault condition is indicated by a logical "L" | | | 11 11,2 3.31 | 1 | |
| | Frigger Voltage Range VTR Switching behaviour is not influenced by trigger quality | | | | 3-10 | VDC | |
| | Dimensions | Standard housing | | | Please contact the | | |
| HOUSING | | | Devices with option CF, non-isolated cooling fins | | | manufactured! | mm³ |
| | | Devices with option DLC | | | | | |
| | Weight | Standard housing | | | Please contact the | | |
| | | Devices with option CF, non-isolated cooling fins Devices with option DLC | | | manufactured! | g | |
| | Devices with option DLC Control Signal Input Pin 1 / Yellow. TTL compatible with Schmitt-Trigger characteristics. Control voltage 2-10 \ | | | | | V /3.5. V recommended for low iitter) | |
| | | readed insert) on bottom side. | | | | | |
| SI | | - | | | | | |
| 0 | 5V Auxiliary Supply Fault Signal Output Pin 3 / Red. The 5 V input is used for rep rates up to the specified max. frequency f _(max) . Full Signal Output Pin 4 / Orange. TTL output, short circuit proof. Indicating switch & driver over-heat, over- | | | | | | |
| FUNCTIONS | Inhibit Signal Input Pin 5 / Green. TTL compatible, Schmitt-Trigger characteristics for the connection of exter | | | | | | |
| | LED Indicators GREEN: "Auxiliary power good, switch OFF". YELLOW: "Control signal received, switch | | | | | | |
| | Temperature Protection A) Standard switches and switches with option CF, GCF: Thermo trigger 75°C, response times the control of the control | | | | | | ver |
| | protection. B) Switches with option DLC: 65°C, response time < 3 s @ 3xPd(max), Δ T=25K (40 | | | | | | |
| | HTS 160-200 SCR Thyristor Switch, 16 kVDC, 2000 ADC Option LP Low Pass. Input filter for increased noise immunity. Option C | | | | | | 3. |
| 377 | | Opti | n S-TT Soft Transition Time. Slower switching speed for simplified EMC. Option | | | | |
| W | | | HFB High Frequency Burst, Improved burst capability by driver. Option HFS High Frequency Switching (two auxiliary supply inputs V1 & V2) Option | | | | |
| JER | | | ption UFTR Ultra Fast Thermotrigger. Response time for shut down < 5s. Option | | | | |
| ORDERINGTI | Option UFTS Ultra Fast Thermosensor. Response time < 5s. NTC 10k / ± 1% Option | | | | | DLC Direct Liquid Cooling (for FPE/PFC). P _{d(max)} can be increased by the factor 1 | |
| | | FOR FURTHER PRODUCT OPTIONS PLEASE REFER TO THE OPTION | | | | | |
| Cust | omized switching units are available on request. All | rdata and sp | ecifications subject to change withou | it notice. Please visit | www.behlke.com for u | ıp-dates. Revision 29.08.2019 ©2017 All rights re | eserved |