	Specification		Symbol	Condition / Comment					HTS 151-20 LC2 Unit					
	Maximum Operating Voltage		$V_{O(max)}$	I <sub>off</sub> < 50 μADC, T <sub>case</sub> = 70°C							15		kVDC	
	Maximum Isolation Voltage		Vı				GND, continuo	usly			± 40		kVDC	
GS	Max. Housing Insulation Voltage		V <sub>INS</sub>	Between s			ace, 3 minutes				± 40		kVDC	
RATINGS	Maximum Turn-On Peak Current		$I_{P(max)}$	$T_{case} = t_p < 200 \mu s$ , duty cyc							200			
747				25°C	uty cycle <					100		ADC		
				t <sub>p</sub> < 10 ms, duty cycle							59			
2			_	t <sub>p</sub> < 100 ms, duty cycle <1%					36		1.50			
MAXIMUM	Maximum Continuous Load Current		I <sub>L(max)</sub>	T <sub>case</sub> =		Standard devices					0.85		ADC	
	Max. Continuous Power Dissipation		P <sub>d(max)</sub>	25°C Devices with option DLC  Tcase = Standard devices & FC, forced air 4 m		2/2			8.25 15		+			
	iviax. Continuous Power Dissipation		r d(max)	25°C	Devices wit	,				1500		Watt		
7	Linear Derating			Above Standard devices & FC, forced air 4 m/s						0.28		1100		
70				25°C Devices with option DLC				26		W/K				
ABSOLUTE	Operating Temperature Range		To	Standard devices & options CF, GCF, ILC. (Option DLC)					-4070		C°			
4	Storage Temperature Range		Ts	Switches with option ILC may require frost protection!					-4090		C°			
	Max. Permissible Magnetic Field		В	Homogeneous steady-field, surrounding the whole switch						25		mT		
	Max. Auxilliary Voltage		V <sub>aux</sub>	Built-in overvoltage limiter (replaceable)						5		VDC		
	Permissible Operating Voltage Range		Vo							0 ± 15		kVDC		
	Typical Breakdown Voltage		$V_{br}$		s a test parar			> 0.5 mA			16		kVDC	
	Typical Off-State Current		I <sub>off</sub>	control purposes only. Not applicable 0.8xV <sub>O</sub> , T <sub>case</sub> =2570°C, reduced							20		µADC	
	Typical Turn-On Resistance		R <sub>stat</sub>	Each switching path			0.1 x I <sub>P(max)</sub> , T <sub>case</sub> =25°C				3.04		1 1 1 1	
	,,				< 1µs, duty cycle < 1% 1.0 x I <sub>P(max)</sub> , T <sub>case</sub> =25°C						4			
				1.0 x I <sub>P(max)</sub> , T <sub>case</sub> =70°C						<u> </u>	8.5		Ohm	
	Typical Propagation Delay Time		t <sub>d(on)</sub>	Resistive load, 0.1 x I <sub>P(max)</sub> , 0.8 x V <sub>C</sub>							250		ns	
	Typical Output Pulse Jitter		tj	Impedance matched input, Vau			V <sub>ctrl</sub> = 5.00 VDC				3		ns	
	Typical Turn-On Rise Time		t <sub>r(on)</sub>	Resistive lo	oad, 10-90%			$1 \times V_{O(max)}$ , $I_L = 0.1 \times I_{p(max)}$			20			
CTERISTICS				Resistive load, 10-90%			$0.8 \times V_{O(max)}$ , $I_L = 0.1 \times I_{p(max)}$				30			
							$0.8 \times V_{O(max)}$ , $I_L = 1.0 \times I_{p(max)}$				40		ns	
	Typical Turn-Off Rise Time		$t_{\text{off,}}t_{q}$				$0.1 \times V_{O(max)}$ , $I_L = 0.1 \times I_{p(max)}$				40		ns	
	Maximum Turn On Time			No limitation			$0.8 \times V_{O(max)}$ , $I_L = 1.0 \times I_{p(max)}$			80 ∞		+		
RIS	Maximum Turn-On Time Minimum Turn-On Time		ton(max)	No limitation							150		ne	
TE	Maximum Turn-Off Time		ton(min)	t <sub>on(min)</sub> can be customized. Please consult factory  No limitation					∞		ns			
AC	Minimum Turn-Off Time		toff(min)	toff(min) can be customized. Please consult factory						150		ns		
CHARA	Max. Continuous Switch	f <sub>(max)</sub>	@ V <sub>aux</sub> = 5.			andard devices without HFS option				<10		110		
CH	Frequency	9	·(max)	Sw. shutdown if f <sub>imax</sub> is  Standard devices with HFS supply						40				
7	, ,		exceeded Opt. HFS + sufficient cooling option						70		kHz			
ELECTRICAL	Maximum Burst Frequency		f <sub>b(max)</sub>	Use option HFB for >10 pulses within 20µs or less						500		kHz		
	Maximum Number of Pulses / Burst		N <sub>(max)</sub>	@ f <sub>b(max)</sub>	'	Standard			10		Pulses			
				Note: Option HFB requires external buffer capacitors			s with a voltage Option I-HFB			>100				
EL				rating of > 630VI	rating of > 630VDC and a cpacitance of 100nF per additional Option HFB						>1000			
	Coupling Capacitance		Cc	HV side against control side					<100		pF			
	Natural Capacitance		CN	Between switch poles, @ 0.5 x Vo <sub>(max)</sub> The V <sub>otrl</sub> has no impact on the output pulse shape.					26		pF			
	Control Voltage Range		V <sub>ctrl</sub>								3 10		VDC	
	Auxiliary Supply Voltage Range		Vaux		,		the HFS mode				5 170		VDC	
	Typical Auxiliary Supply Current		l <sub>aux</sub>		$V_{\text{aux}} = 5.00 \text{ VDC}$ , $T_{\text{case}} = 25^{\circ}\text{C}$ . 0.01 x $f_{\text{(max)}}$ Active current limitation above 1A. (@ $f_{\text{(max)}}$		ax)		800		mADC			
	Fault Signal Output			Active current limitation above 1A. @ f <sub>(max)</sub> Switch will be turn off, if f>f <sub>(max)</sub> , V <sub>aux</sub> <4.75V or T <sub>case</sub> >75°C				H=4V. L=0.5V		VDC				
	Fault Signal Output			Fault condition is indicated by a logical "L"						11-40, L-0.00		\ \bo		
	Opt. HFS, Ext. Supply Voltage V1		V <sub>HFS(V1)</sub>	Stability ±3%, current consumption <0.4 mA/kHz @ 25°C						15		VDC		
	Opt. HFS, Ext. Supply Voltage V2		V <sub>HFS(V2)</sub>	Stability ±3%, current consumption <0.9 mA/kHz @ 25°C						TBD		VDC		
	Intrinsic Diode Forward Voltage		VF	$T_{case} = 25^{\circ}C, I_F = 0.3 \times I_{P(max)}$						<26		VDC		
	Diode Reverse Recovery Time		t <sub>rrc</sub>	$T_{case} = 25$ °C, $I_F = 0.3 \text{ x } I_{P(max)}$ , $di/dt = 100 \text{ A/}\mu\text{s}$						<250		ns		
	Dimensions		LxWxH		Standard housing						Please contact the			
VG					Devices with option CF, non-isolated cooling fins						manufactured! mm			
ISI	14/ 1 / /			Devices with option DLC										
HOUSING	Weight			tandard housing levices with option CF, non-isolated cooling fins levices with option DLC						Please contact the				
4										manufactured!		g		
	Control Signal Input							\/ /2 F \/ rassm	manded for low litter		.1			
	• .	npatible with Schmitt-Trigger characteristics. Control voltage 2-10 V (3-5 V recommended for low jitter).												
S	Logic GND / 5V Return Fin 2 / Black. The ground pin is internally connected with the safety earthing terminal (the pin 3 / Red. The 5 V input is used for rep rates up to the specified max. frequency f <sub>(max)</sub> .													
0	Fault Signal Output  Pin 3 / Red. The 5 vinput is used for repirates up to the specified max. frequency f <sub>(max)</sub> , representations and the specified max. Trequency f <sub>(max)</sub> , representations and the specified max. Trequency f <sub>(max)</sub> , representations are specified max. Trequency f <sub>(max)</sub> ,											sud+		
FUNCTIONS	Inhibit Signal Output Pin 4 / Orange. TTL output, short circuit proof. Indicating switch & driver over-heat, over- Inhibit Signal Input Pin 5 / Green. TTL compatible, Schmitt-Trigger characteristics for the connection of exter													
Š	LED Indicators    Pin 5 / Green.   The compatible, Schmitt-Ingger characteristics for the connection of extermine													
¥	Temperature Protection	d switches with option CF, GCF: Thermo trigger 75°C, response time < 60 s @ $3xPd(max)$ , $\Delta T=25K$ (50 to 75°C). Separate driver								ıρr				
	remperature Frotection							_	o 65°C), coolant flow > 3I / min. Separate driver protection.					
	HTS 151-20 LC2 Transistor Swit	tch, 15 kVDC, 200 AE		on LP Low Pass. Input filter for increased noise immunity. Option C										
ORDERINGTI	TIGHTSISIUL SWI		tion S-TT Soft Transition Time. Slower swi			·				ange Housing. $P_{d(max)}$ can be inc				
		Optio	on HFB High	High Frequency Burst, Improved burst capability by driver. Option (					CF Copper Cooling Fins. P <sub>d(max)</sub> can be increased by the factor 3 to 10.					
ER				High Frequency Switching (two auxiliary supply inputs V1 & V2 ) Option  Ultra Fast Thermotrigger. Response time for shut down < 5s. Option										
RD										Cooling (for FPE/PFC). Pd(max) can be Cooling (for FPE/PFC). Pd(max) can				
0		FOR FURTHER PRODUCT OPTIONS PLEASE REFER TO THE OPTION:												
		ble on request. All o	lata and en	acifications su	bioot to obonge	without not	ico Ploseo vieit w	www.boblko.co	om for u	p-dates.	Revision 14.03.	2019 ©2018 All rights re	and the same of	