	Specification		Symbol	Condition / Comment				HTS 151-10-AC	Unit	
	•		V _{O(max)}	l _{off} < 50 μADC, T _{case} = 70°C					kVDC	
6	Maximum Operating Voltage Maximum Isolation Voltage		V _{O(max)}	Between HV switch and control / GND, continuously				± 15	kVDC	
Š	Maximum Isolation Voltage Max. Housing Insulation Voltage		V _{INS}					± 40	kVDC	
RATINGS	Maximum Turn-On Peak Current		· · · · · · · · · · · · · · · · · · ·	Between switch and housing sur $T_{case} = 25^{\circ}C$ $t_p < 200 \mu s$, d		uty cycle <1%		± 40 100	KVDC	
8	Maximum Continuous Load Current		I _{P(max)}	I case - 23 C				59	ADC	
					t_p < 1 ms, duty cycle <1% t_p < 10 ms, duty cycle <1%			36	ADC	
Ž				t _p < 100 ms, duty cy				27		
X			I _{L(max)}	Standard davisos			0.6	ADC		
MAXIMUM	MAZETIGITI COTILITUOUS ECAU CUITETIL		IL(max)	T _{case} = 25°C Devices with option DLC		3	ADC			
	Max. Continuous Power Dissipation		P _{d(max)}	Standard devices & FC, forced air 4 m/s		12.5	+			
5	Commiddad i Offor Dissipation		i d(max)	T _{case} = 25°C Devices with option DLC		1500	Watt			
70	Linear Derating			Standard devices & FC, forced air 4 m/s		0.28	+			
4BSOLUTE	Ented Bolding			Above 25°C Devices with option DLC		2	W/K			
4	Operating Temperature Range		To	Standard devices & options CF, GCF, ILC. (Option DLC)		-4070	C°			
	Storage Temperature Range		Ts	Switches with opti				-5090	C°	
	Permissible Operating Voltage Range		Vo	, , , ,		0 ± 15	kVDC			
	Typical Breakdown Voltage		V _{br}	NOTE: V _{br} is a test parameter for quality control purposes only. I off > 0.5 mA		16.5	kVDC			
	Typical Off-State Current		I _{off}	0.8xV _O , T _{case} =2570°C, reduced l _{off} on request		20	μADC			
	Typical Turn-On Resistance		R _{stat}	Each switching path 0.1 x I _{P(max)} , T _{case} =25°C		6.86	+			
	,			$t_p < 1\mu s$, duty cycle < 1% 1.0 x $I_{P(max)}$, $T_{case} = 25$ °C			10.3			
						1.0 x I _{P(max)} ,	√Case =70°C	27.4	Ohm	
	Residual Voltage (Total Voltage drop on-state)		V _{res}	T _{case} = 25°C			I _L = 0.001 A	0.012	1	
							$I_{L} = 0.01 A$	0.12		
							I _L = 0.1 A	1.2	VDC	
							I _L = 1.0 A	16		
CHARACTERISTICS							$I_{L} = 10.0 \text{ A}$			
	Typical Propagation Delay Time		$t_{d(on)}$	Resistive load, 0.1 x I _{P(max)} , 0.8 x V _{O(max)} , 50-50%		250	ns			
	Typical Output Pulse Jitter		tj	Impedance matched input, V _{aux} /				3	ns	
	Typical Turn-On Rise Time)	$t_{r(on)}$	Resistive load, 10	-90%		$I_L = 0.1 \times I_{p(max)}$	TBD		
							$I_L = 0.1 \times I_{p(max)}$			
							$I_L = 1.0 \times I_{p(max)}$		ns	
	Typical Turn-Off Rise Time		$t_{\text{off,}}t_{\text{q}}$	Resistive load, 10-90% $0.1 \times V_{O(max)}, I_L = 0.1 \times I_{p(max)}$			40	ns		
7				$0.8 \times V_{O(max)}$, $I_L = 1.0 \times I_{p(max)}$			90			
40	Maximum Turn-On Time		$t_{\text{on(max)}}$	No limitation				∞		
A	Minimum Turn-On Time		t _{on(min)}	t _{on(min)} can be customized. Please consult factory				120	ns	
CH	Minimum Turn-Off Time		$t_{\text{off}(\text{min})}$	t _{off(min)} can be customized. Please consult factory				120	ns	
7	Max. Continuous Switching Frequency		$f_{(max)}$	@ V _{aux} = 5.00 V Standard devices without HFS option				TDB		
2				Sw. shutdown if f _(max) is Standard devices with HFS supply Ont HFS a sufficient cooling option				50		
ECTRICAL				exceeded Opt. HFS + sufficient cooling option				100	kHz	
	Maximum Burst Frequency		f _{b(max)}	Use option HFB for >10 pulses within 20µs or less				500	MHz	
	Maximum Number of Pulses / Burst		$N_{(max)}$	@ f _{b(max)} Standard				10 Use option HFB for >10	Pulses	
'				Note: Option HFB requires external buffer capacitors with a voltage rating of > 630VDC and a opacitance of 100nF per additional Option HFB Option HFB				>100		
			_					>1000		
	Coupling Capacitance		Cc	AV side against control side				<25	pF	
	Natural Capacitance		C _N	Between switch poles, @ 0.5 x V _{O(max)}				<50	pF	
	Control Voltage Range		V _{ctrl}	The V _{ctrl} has no impact on the output pulse shape.				2 6	VDC	
	Auxiliary Supply Voltage Range		V _{aux}	The +5 V supply is not required in the HFS mode.				5	VDC	
	Typical Auxiliary Supply Current		l _{aux}	V _{aux} = 5.00 VDC,			0.01 x f _(max)	TBD	400	
	5 1101 10 1			Active current limita			@ f _(max)	800	mADC	
	Fault Signal Output			Switch will be turn			case>/5°C	H=4V, L=0.5V	VDC	
	Ont HES Ext Supply Voltage V1		17	Fault condition is indicated by a logical "L" Stability +3% current consumption <0.4 mA/kHz @ 25°C				45	VDC	
	Opt. HFS, Ext. Supply Voltage V2		V _{HFS(V1)}	Stability ±3%, current consumption <0.4 mA/kHz @ 25°C				15 TPD	VDC	
	Opt. HFS, Ext. Supply Voltage V2 Intrinsic Diode Forward Voltage		V _{HFS(V2)}	Stability ±3%, current consumption <0.9 mA/kHz @ 25°C				TBD	VDC	
	Diode Reverse Recovery Time		V _F	$T_{case} = 25^{\circ}\text{C}, \ I_F = 0.3 \text{ x } I_{P(max)}$ $T_{case} = 25^{\circ}\text{C}, \ I_F = 0.3 \text{ x } I_{P(max)}, \ di/dt = 100 \ A/\mu s$				<10 <255	VDC	
	Dimensions		t _{rrc} LxWxH	I case = 25°C, I _F = 0.3 x I _{P(max)} , dt/dt = 100 A/μs Standard housing				<255 175x75x56	ns	
th.	HIGHSIOUS			Devices with option CF, non-isolated cooling fins					mm ³	
Ž				Devices with option		nated cooling in	Please contact the	1111111		
HOUSING	Weight			Standard housing	III DEG		manufactured!	-		
9	vveignt			Devices with option CF, non-isolated cooling fins				Please contact the	_	
•				Devices with option		nated cooling in	manufactured!	g		
	Control Signal Input Pin 1 / Yellow. TTL cor			atible with Schmitt-Trigger characteristics. Control voltage 2-10 V (3-5 V recommended f				for low jitter)		
	ogic GND / 5V Return ogic GND / 5V Return Pin 2 / Black. The ground pin is internally connected with the safety earthing terminal (threaded insert) on bot									
S										
FUNCTIONS	5V Auxiliary Supply Pin 3 / Red. The 5 V input is used for rep rates up to the specified max. frequency f _(max) . Higher rep rates require Pin 4 / Orange TTL output short circuit proof Indicating switch & driver over-frequency low auxiliary.									
	Fault Signal Output Pin 4 / Orange. TTL output, short circuit proof. Indicating switch & driver over-heat, over-frequency, low auxiliary Inhibit Signal Input Pin 5 / Green TTL compatible. Schmitt-Trigger characteristics for the connection of external safety circuits L = S							_		
	hibit Signal Input Pin 5 / Green. TTL compatible, Schmitt-Trigger characteristics for the connection of external safety circuits. L = S ED Indicators CREEN: "Auxiliary power good quiteb OFE" VELLOW: "Control circuit received quiteb ON" RED: "Fault oper									
				=	ood, switch OFF". YELLOW: "Control signal received, switch ON". RED: "Fault condition, switch OFF"					
	Temperature Protection	· ·	witches with option CF, GCF: Thermo trigger 75°C, response time < 60 s @ 3xPd(max), ∆T=25K (50 to 75°C). Separate driver protection. B) Switches with							
		time < 3 s @ $3xPd(max)$, $\Delta T=25K$ (40 to $65^{\circ}C$), coolant flow > $3l$ / min. Separate driver protect on LP Low Pass. Input filter for increased noise immunity. Option CCF Cerar								
6	HTS 151-150 AC Transistor Swit				•	amic Flange Housing. P _{d(max)} can be increased by the factor 3 to 1	15.			
N.						per Cooling Fins. P _{d(max)} can be increased by the factor 3 to 10. Inded Cooling Flange (copper). P _{d(max)} can be increased by the factor 3 to 15.				
EF				tion HFS High Frequency Switching (two auxiliary supply inputs V1 & V2) Option ILC Indirect				ect Liquid Cooling (for water). P _{d(max)} can be increased by the factor 3 to 15.		
ORDERING	0			ion CCS Ceramic Cooling Surface. P _{d(max)} can be increased by the factor 2 Option DLC Direct				t Liquid Cooling (for FPE/PFC). P _{d(max)} can be increased by the factor 10 to 100. 15.		
				FOR FURTHER PRODUCT OPTIONS PLEASE REFER TO THE OPTIONS PAGE.						
Custo	omized switching units are availab	ole on request. All	data and sp	pecifications subject to o	change without n	otice. Please visit	www.behlke.com for up-dates.	Revision 19-02-2019 ©2017 All rights re	eserved	