Maximum Soletion Votinge Warene W	(Let	Unit
Max. Housing Insulation Vollage V _{xx} Between HV switch and control of GND, continuously x + 40 Max. Housing Insulation Vollage V _{xx} Between HV switch and control of GND, continuously x + 50 Max. Housing Insulation Vollage V _{xx} Exhance switch and housing surface. 3 minutes x + 50 Max. Housing Insulation Vollage V _{xx} V _{xx}	.0	kVDC
Maximum Turn-On Peak Current Impact		kVDC
Maximum Turn-On Peak Current Imprest Imp		kVDC
Maxmum Continuous Loed Current Umbel Variety Var		KVDO
Maxmum Continuous Loed Current Umbel Variety Var		
Maxmum Continuous Loed Current Umbel Variety Var		
Max. Continuous Prover Disspeation Papers Table 25°C Standard devices & PC, forced at 4" mis 0.69		ADC
Max. Continuous Prover Disspeation Papers Table 25°C Standard devices & PC, forced at 4" mis 0.69		
Max. Continuous Prover Disspeation Papers Table 25°C Standard devices & PC, forced at 4" mis 0.69		ADC
Max. Continuous Prover Dissipation Prince 25°C Standard devices & Prince 32		
Above 25°C Devices with option DLC 89		
Above 25°C Standard cerices & Ft., Incred air 4 ms Dusy Devices with option ID.C 89		Watt
Storage Temperature Range T _S Switches with option ILC may require frost protection! 4-090		
Storage Temperature Range Ts Switches with option ILC may require frost protection! 4090		W/K
Max. Auxiliary Voltage Vax Bulkin overvoltage limiter (replaceable) 5.5		°C
Max. Auxilliary Voltage		°C
Permissible Óperating Voltage Range Range Range Typical Breakdown Voltage Typical Breakdown Voltage Typical Off-State Current I lar O BXVO, Tase = 257.07°C, reduced tar for request Typical Turn-On Resistance I lar O BXVO, Tase = 257.07°C, reduced tar for request Typical Capacitive Power Typical Turn-On Resistance Raze Each switching path t ₅ < 1 µs, duty cycle < 1 ½ h. 10. x lmpach, Tase = 25°C 1. 2 1.0 x lmpach, Tase = 25°C 1.0 x lmpach, T		mT
Range		VDC
Typical Breakdown Voltage Var NoTE: V _v is a last parameter for quality control purposes only, Not applicable in normal operation! Int 0.8xV ₀ , T _{come} = 2570°C, reduced l _{st} on request 50		kVDC
Typical Off-State Current Iust 0.8 kV Creamy Each switching path 0.1 x Ingress 25°C 0.3		
Typical Off-State Current Iuf 0.8 V/o, Tase = 25. 70°C, reduced Les on request 50		kVDC
Typical Turn-On Resistance Retat Each switching path to 1 y large part 1.0 x large part		μADC
Typical Capacitive Power Past Switch capacitances only- Without external load and parasitic capacitances only- Without external load and parasitic capacitances O.8 x V _{Comod} , f = 100Hz O.038 O.38 x V _{Comod} , f = 100Hz O.038 O.38 x V _{Comod} , f = 100Hz O.038 O.38 x V _{Comod} , f = 100Hz O.038 O.38 x V _{Comod} , f = 100Hz O.38 O.38 x V _{Comod} , f = 10Hz O.38 O.38 x V _{Comod} , f = 10Hz		
Typical Capacitive Power Dissipation of Switch (Natural Power Dissipation of Switch (Natural Power Dissipation of Switch (Natural Power Dissipation Polary Time Interest Program		
Dissipation of Switch (Natural Power Dissipation) without external load and parasitic capacitances 0.8 x V _{O(max)} , f = 1000Hz 0.32		Ohm
(Natural Power Dissipation) parasitic capacitances! 0.8 x V _{O(max)} , f = 10000Hz 0.32		
Typical Propagation Delay Time t _(min) Resistive load, 0.1 x I _{P(max)} , 0.8 x V _{O(max)} , 50-50% TBD Typical Output Pulse Jitter t _(min) Impedance matched input, V _{aux} I V _{est} = 5.00 VDC 2 Typical Output Transition Time (Rise Time & Fall Time) Maximum Turn-On Time t _(min) No limitation No		Watt
Typical Output Pulse Jitter Typical Output Transition Time (Rise Time & Fall Time) Maximum Turn-On Time tor(max) No limitation Can be customized. Please consult factory 180		
Typical Ouput Transition Time (Rise Time & Fall Time) Typical Ouput Transition Time (Rise Time & Fall Time) Typical Ouput Transition Time (Rise Time & Fall Time) Typical Ouput Transition Time (Rise Time & Fall Time) Typical Ouput Transition Time (Rise Time & Fall Time) Typical Ouput Transition Time (Rise Time & Fall Time) Typical Ouput Transition Time (Rise Time & Fall Time) Typical Ouput Transition Time (Rise Time) Typical Ouput Typical Auxiliary Supply Voltage V1 (Vies Vir) (Stability ±3%, current consumption <0.4 ma/kHz @ 25°C Tibo Opt. Hrs, Ext. Supply Voltage V1 (Vies Vir) (Stability ±3%, current consumption <0.5 ma/kHz @ 25°C Tibo Opt. Hrs, Ext. Supply Voltage V1 (Vies Time) Typical Auxiliary Supply Voltage V2 (Vies Vir) (Stability ±3%, current consumption <0.5 ma/kHz @ 25°C Tibo Opt. Hrs, Ext. Supply Voltage V2 (Vies Vir) (Stability ±3%, current consumption <0.5 ma/kHz @ 25°C Tibo Opt. Hrs, Ext. Supply Voltage V2 (Vies Vir) (Stability ±3%, current consumption <0.5 ma/kHz @ 25°C Tibo Opt. Hrs, Ext. Supply Voltage V2 (Vies Vir)		ns
Control Voltage Range Volt The Volt Sandard devices with poles, @ 0.5 x Voltas) Control Voltage Range Volt The Volt Active courrent imitation above 1A.		ns
Maximum Turn-On Time Lon(max) No limitation Sax Vo(max), It. = 1.0 x I _{p(max)} 60		
Maximum Turn-On Time ton(max) No limitation can be customized. Please consult factory 180		
Minimum Turn-On Time Max. Continuous Switching Max. Switching Max. Continuous Switching Max.		ns
Max. Continuous Switching frmax (ns
Frequency Sw. shutdown if fimacy is exceeded Sw. shutdown if fimacy is exceeded Opt. HFS + sufficient cooling option 80		ns
Maximum Number of Pulses / Burst N _(max) f _b -1MHz (1µs spacing). Switch shutdown if N _(max) is exceeded. 10 Use Coupling Capacitance Cc Switch against control side Standard devices & options CF, DLC < 150 Natural Capacitance Cn Between switch poles, @ 0.5 x Vo _(max) < 20 Control Voltage Range V _{ctrl} The V _{drl} has no impact on the output pulse shape. 3 10 Auxiliary Supply Voltage Range V _{Bux} The +5 V supply is not required in the HFS mode. 4.5 5.5 Typical Auxiliary Supply Current I _{aux} The +5 V supply is not required in the HFS mode. 0.01 x f _(max) Opt. HFS, Ext. Supply Voltage V1 V _{HFS(V1)} Stability ±3%, current consumption <0.4 mA/kHz @ 25°C 15 Opt. HFS, Ext. Supply Voltage V2 V _{HFS(V2)} Stability ±3%, current consumption <0.5 mA/kHz @ 25°C TBD Intrinsic Diode Forward Voltage V _F T _{case} = 25°C, I _F = 0.3 x I _{P(max)} <80 Diode Reverse Recovery Time t _{rrc} T _{case} = 25°C, I _F = 0.3 x I _{P(max)} , dil/dt = 100 A/µs Please contact the manufactured! Weight Standard housing periods with option ILC & DLC Please contact the manufactured! <t< th=""><td></td><td></td></t<>		
Maximum Number of Pulses / Burst N _(max) f _b -1MHz (1µs spacing). Switch shutdown if N _(max) is exceeded. 10 Use Coupling Capacitance Cc Switch against control side Standard devices & options CF, DLC < 150 Natural Capacitance Cn Between switch poles, @ 0.5 x Vo _(max) < 20 Control Voltage Range V _{ctrl} The V _{drl} has no impact on the output pulse shape. 3 10 Auxiliary Supply Voltage Range V _{Bux} The +5 V supply is not required in the HFS mode. 4.5 5.5 Typical Auxiliary Supply Current I _{aux} The +5 V supply is not required in the HFS mode. 0.01 x f _(max) Opt. HFS, Ext. Supply Voltage V1 V _{HFS(V1)} Stability ±3%, current consumption <0.4 mA/kHz @ 25°C 15 Opt. HFS, Ext. Supply Voltage V2 V _{HFS(V2)} Stability ±3%, current consumption <0.5 mA/kHz @ 25°C TBD Intrinsic Diode Forward Voltage V _F T _{case} = 25°C, I _F = 0.3 x I _{P(max)} <80 Diode Reverse Recovery Time t _{rrc} T _{case} = 25°C, I _F = 0.3 x I _{P(max)} , dil/dt = 100 A/µs Please contact the manufactured! Weight Standard housing periods with option ILC & DLC Please contact the manufactured! <t< th=""><td></td><td>kHz</td></t<>		kHz
Maximum Number of Pulses / Burst N _(max) f _b -1MHz (1µs spacing). Switch shutdown if N _(max) is exceeded. 10 Use Coupling Capacitance Cc Switch against control side Standard devices & options CF, DLC < 150 Natural Capacitance Cn Between switch poles, @ 0.5 x Vo _(max) < 20 Control Voltage Range V _{ctrl} The V _{drl} has no impact on the output pulse shape. 3 10 Auxiliary Supply Voltage Range V _{Bux} The +5 V supply is not required in the HFS mode. 4.5 5.5 Typical Auxiliary Supply Current I _{aux} The +5 V supply is not required in the HFS mode. 0.01 x f _(max) Opt. HFS, Ext. Supply Voltage V1 V _{HFS(V1)} Stability ±3%, current consumption <0.4 mA/kHz @ 25°C 15 Opt. HFS, Ext. Supply Voltage V2 V _{HFS(V2)} Stability ±3%, current consumption <0.5 mA/kHz @ 25°C TBD Intrinsic Diode Forward Voltage V _F T _{case} = 25°C, I _F = 0.3 x I _{P(max)} <80 Diode Reverse Recovery Time t _{rrc} T _{case} = 25°C, I _F = 0.3 x I _{P(max)} , dil/dt = 100 A/µs Please contact the manufactured! Weight Standard housing periods with option ILC & DLC Please contact the manufactured! <t< th=""><td></td><td>kHz</td></t<>		kHz
Coupling Capacitance Control Side Natural Capacitance C _N Between switch poles, @ 0.5 x V _{0(max)} < 20 Control Voltage Range V _{ctrl} The V _{ctrl} has no impact on the output pulse shape. 3 10 Auxiliary Supply Voltage Range V _{aux} The +5 V supply is not required in the HFS mode. 4.5 5.5 Typical Auxiliary Supply Current I _{aux} V _{aux} = 5.00 VDC, T _{case} = 25°C. 0.01 x f _(max) TBD Active current limitation above 1A. @ specified f _(max) 400 Opt. HFS, Ext. Supply Voltage V1 V _{HFS} (V1) Stability ±3%, current consumption <0.4 mA/kHz @ 25°C 15 Opt. HFS, Ext. Supply Voltage V2 V _{HFS} (V2) Stability ±3%, current consumption <0.5 mA/kHz @ 25°C TBD Intrinsic Diode Forward Voltage V _F T _{case} = 25°C, _F = 0.3 x _{F(max)} , di/dt = 100 A/µs <40 V _H Standard housing V _H Standard housi	se burst option HFB for >10 pulses	Pulses
Control Side	o barot option in Bion in paloco	pF
Control Voltage Range Vetrox The vetro		P .
Auxiliary Supply Voltage Range Typical Auxiliary Supply Current Vaux The +5 V supply is not required in the HFS mode. 4.5 5.5 Typical Auxiliary Supply Current Vaux Vaux = 5.00 VDC, T _{case} = 25°C. Opt. HFS, Ext. Supply Voltage V1 VHFS(V1) Stability ±3%, current consumption <0.4 mA/kHz @ 25°C TBD Opt. HFS, Ext. Supply Voltage V2 VHFS(V2) Intrinsic Diode Forward Voltage VF T _{case} = 25°C, I _F = 0.3 x I _{P(max)} , di/dt = 100 A/µs Vaux VHFS(V1) Stability ±3%, current consumption <0.5 mA/kHz @ 25°C TBD Intrinsic Diode Forward Voltage VF T _{case} = 25°C, I _F = 0.3 x I _{P(max)} , di/dt = 100 A/µs Vaux VHFS(V1) VHFS(V2) Stability ±3%, current consumption <0.5 mA/kHz @ 25°C TBD Intrinsic Diode Forward Voltage VF T _{case} = 25°C, I _F = 0.3 x I _{P(max)} , di/dt = 100 A/µs Vaux VHFS(V1) VHFS(V2) Stability ±3%, current consumption <0.5 mA/kHz @ 25°C TBD Intrinsic Diode Forward Voltage VF T _{case} = 25°C, I _F = 0.3 x I _{P(max)} , di/dt = 100 A/µs Vaux VHFS(V1) VHFS(V2) Stability ±3%, current consumption <0.5 mA/kHz @ 25°C TBD Intrinsic Diode Forward Voltage VF T _{case} = 25°C, I _F = 0.3 x I _{P(max)} , di/dt = 100 A/µs Vaux VHFS(V1) VHFS(V1) VHFS(V1) VHFS(V2) Stability ±3%, current consumption <0.4 mA/kHz @ 25°C TBD Intrinsic Diode Forward Voltage VF T _{case} = 25°C, I _F = 0.3 x I _{P(max)} , di/dt = 100 A/µs VAUX VHFS(V2)		pF
Typical Auxiliary Supply Current Iaux		VDC
Active current limitation above 1A. @ specified f _(max) 400 Opt. HFS, Ext. Supply Voltage V1 V _{HFS(V1)} Stability ±3%, current consumption <0.4 mA/kHz @ 25°C 15 Opt. HFS, Ext. Supply Voltage V2 V _{HFS(V2)} Stability ±3%, current consumption <0.5 mA/kHz @ 25°C TBD Intrinsic Diode Forward Voltage V _F T _{case} = 25°C, I _F = 0.3 x I _{P(max)} < 80 Diode Reverse Recovery Time t _{rrc} T _{case} = 25°C, I _F = 0.3 x I _{P(max)} , di/dt = 100 A/µs <40 Dimensions LxWxH Standard housing, without pigtails Please contact the Devices with option ILC & DLC manufactured! Weight Standard housing Devices with option ILC & DLC Please contact the manufactured! Control Signal Input Logic GND / 5V Return Pin 2 / Black (LS-C: Pin 1). TTL compatible (LS-C: With 100Ω termination). Schmitt-Trigger characteristics. Control volta pin is internally connected with the safety earthings terminals (threaded inser		VDC
Opt. HFS, Ext. Supply Voltage V1 VHFS(V1) Stability ±3%, current consumption <0.4 mA/kHz @ 25°C		
Opt. HFS, Ext. Supply Voltage V2 VHFS(V2) Stability ±3%, current consumption <0.5 mA/kHz @ 25°C		mADC
Intrinsic Diode Forward Voltage V _F T _{case} = 25°C, I _F = 0.3 x I _{P(max)} <80 Diode Reverse Recovery Time t _{rrc} T _{case} = 25°C, I _F = 0.3 x I _{P(max)} , di/dt = 100 A/µs <40 Dimensions LxWxH Standard housing, without pigtails Please contact the manufactured! Weight Standard housing Devices with option ILC & DLC Please contact the manufactured! Control Signal Input Logic GND / 5V Return Pin 1 / Yellow (LS-C: Pin 1). TTL compatible (LS-C: With 100Ω termination). Schmitt-Trigger characteristics. Control volta Pin 2 / Black (LS-C: Shielding). The ground pin is internally connected with the safety earthings terminals (threaded inser		VDC
Diode Reverse Recovery Time trc T _{case} = 25°C, I _F = 0.3 x I _{P(max)} , di/dt = 100 A/μs <40 Variable		VDC
Dimensions LxWxH Standard housing, without pigtails Devices with option ILC & DLC Weight Standard housing Devices with option ILC & DLC Please contact the manufactured!		VDC
Devices with option ILC & DLC manufactured!		ns
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Logic GND / 5V Return Pin 2 / Black (LS-C: Shielding). The ground pin is internally connected with the safety earthings terminals (threaded inser		g
		jitter).
Valuation 5 5 5 5 5 5 5 5 5		
, iii	equire option HFS.	
Fault Signal Output Pin 4 / Orange (LS-C: Pin 3). TTL output, short circuit proof. Indicating switch & driver over-heat, over-frequency, low auxiliary and the state of the st	xiliary voltage. L = Fault.	
Inhibit Signal Input Pin 5 / Green (LS-C: Pin 2). TTL compatible, Schmitt-Trigger characteristics for the connection of external safety circuits.	. L = Switch Inhibited.	
LED Indicators GREEN: "Auxiliary power good, switch OFF". YELLOW: "Control signal received, switch ON". RED: "Fault condition, sw	switch OFF"	
Temperature Protection A) Standard switches and switches with opt. FC, CF , GCF: Thermo trigger 75°C, response time < 60 s @ 3xPd(max), ΔT=25	25K (50 to 75°C). Separate	driver
protection. B) Switches with option DLC: 65°C, response time < 3 s @ 3xPd(max), ΔT=25K (40 to 65°C), coolant flow > 3l / min. S	Separate driver protection.	
	nents according to customer specifi	ication.
Option HFB High Frequency Burst (improved capability by external capacitors) Option UL-94 Flame retardant casting Option HFS High Frequency Switching (two auxiliary supply inputs V1 & V2) Option I-FWD Integrated Free-Wheeling I	•	and only
Ontion LHES Integrated High Frequency Burst Ontion LEWDN Integrated Frequency Burst	g Diode. In connection with inductive lo Diode Network. In connection with ind	
Option S-TT Soft Transition Time decrease the rise and fall time by 20% Option PT-C Pigtal for control connection	tion: Flexible leads (I=75mm) with lemo	o connector.
Option Min-On Individually increased "Min. On-Time" to avoid unwanted triggering Option SEP-C Separated control unit. Cor	Control unit with LED indicators in a sep	arate
Option S-TT Option Min-On Individually increased "Min. On-Time" to avoid unwanted triggering Option TH Option Min-Off Option Min-Off Individually increased "Min. Off-Time" to avoid unwanted triggering Option TH Tubular Housing Option PCC Pulser Configuration. Switch combined with custom specific parts. Option CF Copper Cooling Fins. Pd	P _{d(max)} can he increased by the feet	or 3 to 10
Option ISO-80 80kV Isolation. Isolation Voltage increased to 80kV. Option GCF Grounded Cooling Flange.	· · · · · · · · · · · · · · · · · · ·	
Option ISO-120 120kV Isolation. Isolation Voltage increased to 120kV. Option ILC Indirect Liquid Cooling (for	or water). P _{d(max)} can be increased by the	he factor 3 to 1
	(max) can be increased by the factor 10	
Option ISO-200 200kV Isolation. Isolation Voltage increased to 200kV. FOR FURTHER PRODUCT OPTIONS PLI Customized switching units are available on request. All data and specifications subject to change without notice. Please visit www.behlke.com for up-dates. 301-30-GSM-Sid		