|                            | Specification   | Symbol   | Condition / Comment  |   |  |   | 201-1  | 80 SiC   | 201-240 SiC             | 201-300 SiC                   | Unit       |
|----------------------------|---|--|--|---|--|---|--|--|-------------------------|-------------------------------|------------|
|                            | Maximum Operating Voltage   | $V_{O(max)}$   |  |   |  |   |  |  | 20                      |                               | kVDC       |
|                            | Maximum Isolation Voltage   |  |  |   |  |   | ± 30   |  |                         |                               | kVDC       |
| RATINGS                    | Max. Housing Insulation Voltage   | V <sub>INS</sub>   | Between switch and housing surface, 3 minutes  |   |  |   |  | ± 50   |                         | kVDC                          |            |
|                            | Maximum Turn-On Peak Current  | I <sub>P(max)</sub>  | T <sub>case</sub> = 25°C   | t <sub>p</sub> < 200 μs, duty cycle <1%           |  | 18  | 800  | 2400   | 3000                    |                               |            |
|                            |   |  |  |   | t <sub>p</sub> < 1 ms, duty cycle <1%                          |   |  | 000  | 1400                    | 1900                          |            |
|                            |   |  |  |   | t <sub>p</sub> < 10 ms, duty cycle <1%                         |   |  | 340  | 500                     | 500                           | 400        |
| 2                          |   |  |  |   | t <sub>p</sub> < 100 ms, duty cycle <1%                        |   |  | 20   | 240                     | 300                           | ADC        |
| MAXIMUM                    | Maximum Continuous Load Curren  | t I <sub>L(max)</sub>  | T <sub>case</sub> = 25°C   |   | Standard devices   |   | l .  | 7  | 10                      | 38                            | 400        |
|                            | Max. Continuous Power Dissipation   | D  |  | Devices with option DLC                           |  |   |  | 50<br>00   | 80<br>140               | 100<br>210                    | ADC        |
|                            | Iviax. Continuous Power Dissipation   | n P <sub>d(max)</sub>  | T <sub>case</sub> = 25°C   | Standard devices & FC forced air 4 m/s            |  |   |  | 500  | 2000                    | 3000                          | Watt       |
| 5                          | Linear Derating   |  |  |   |  |   |  | .3   | 0.4                     | 0.7                           | watt       |
| 70                         | Emour Bording   |  | Above 25°C Devices with option DLC   |   |  | 33  | 186  | 240  | W/K                     |                               |            |
| ABSOLUTE                   | Operating Temperature Range   | To   | Standard devices & options CF, GCF, ILC. (Option DLC)  |   |  |   |  | -4070 (60)   |                         | °C                            |            |
| 4                          | Storage Temperature Range   | Ts   | Switches with option ILC may require frost protection!   |   |  |   |  | -4090  |                         | °C                            |            |
|                            | Max. Permissible Magnetic Field   | В  | Homogeneous steady-field, surrounding the whole switch   |   |  | hole switch   |  |  | 25                      |                               | mT         |
|                            | Max. Auxilliary Voltage   | V <sub>aux</sub>   | Built-in overvoltage limiter (replaceable)   |   |  |   |  |  | 5.5                     |                               | VDC        |
|                            | Permissible Operating Voltage Range   |  |  |   |  |   |  | 0 ± 20   |                         | kVDC                          |            |
|                            | Typical Breakdown Voltage   | $V_{br}$   | <b>NOTE:</b> V <sub>br</sub> is a test parame purposes only. Not applicable  |   |  |   |  | 22   |                         |                               | kVDC       |
| ELECTRICAL CHARACTERISTICS | Typical Off-State Current   | l <sub>off</sub>   | 0.8xV <sub>O</sub> , T <sub>case</sub> =2570°C, reduced l <sub>off</sub> on  |   |  | est   | < 80   |  |                         |                               | uADC       |
|                            | Typical Turn-On Resistance  | R <sub>stat</sub>  | Each switching path 0.1 x I <sub>P(max)</sub> , T <sub>case</sub> =25°C  |   |  | 0.0   | 067  | 0.05   | 0.04                    |                               |            |
|                            |   |  | t <sub>p</sub> < 1μs, duty cycle < 1%  |   | 1.0 x I <sub>P(max)</sub> , T <sub>cas</sub>                   | 1.0 x I <sub>P(max)</sub> , T <sub>case</sub> =25°C |  | .1   | 0.075                   | 0.0172                        |            |
|                            |   |  |  |   | 1.0 x I <sub>P(max)</sub> , T <sub>case</sub> =70°C            |   | 0  | .2   | 0.15                    | 0.144                         | Ohm        |
|                            | Typical Propagation Delay Time  | t <sub>d(on)</sub>   |  |   | P(max), 0.8 x V <sub>O(max)</sub> , 50-50%                     |   |  |  | 200                     |                               | ns         |
|                            | Typical Output Pulse Jitter t <sub>j</sub>  |  | Impedance matched input, V <sub>aux</sub> / V <sub>ctrl</sub> = 5.00 VDC   |   |  |   |  | 3  |                         | ns                            |            |
|                            | Typical Turn-On Rise Time   | $t_{r(on)}$  | Resistive load, 10-90% 0.1 x $V_{O(max)}$ , $I_L = 0.1$ x $I_{p(max)}$   |   |  |   |  | 30   |                         |                               |            |
|                            |   |  | Resistive load, 10-90%   |   | $\begin{array}{c} 0.8 \text{ x } V_{O(\text{max})},        $   |   |  |  | 45                      |                               | ns         |
|                            | T T   |  |  |   |  |   |  |  | 60                      | 50                            |            |
|                            | Typical Turn-Off Rise Time  | $t_{\text{off}}, t_{\text{q}}$   |  |   |  |   |  |  |                         |                               |            |
|                            |   |  |  |   | $0.8 \times V_{O(max)}, I_L = 1.0 \times I_{p(max)}$           |   | 100  |  |                         |                               | ns         |
|                            | Maximum Turn-On Time  | t <sub>on(max)</sub>   |  |   |  |   |  |  | ∞ 450                   |                               | ns         |
|                            | Minimum Turn-On Time  | ton(min)   | t <sub>on(min)</sub> can be customized. Please consult factory   |   |  |   |  | 150  |                         | ns                            |            |
|                            | Maximum Turn-Off Time Minimum Turn-Off Time   | t <sub>off(max)</sub>  |  |   |  |   |  |  | ∞<br>150                |                               | ns         |
|                            | Max. Continuous Switching   | t <sub>off(min)</sub>  | t <sub>off(min)</sub> can be customized. Please consult factory  @ V <sub>aux</sub> = 5.00 V Standard devices without HFS option                               |   |  |   |  |  | TBD                     |                               | ns         |
|                            | Frequency   | t <sub>(max)</sub>   | Sw. shutdown if f <sub>imax</sub> : Standard devices without Hr 3 option   |   |  |   |  |  | 30                      |                               |            |
|                            | riequency   |  | is exceeded  Opt. HFS + sufficient cooling option  |   |  |   |  |  | 80                      |                               | kHz        |
|                            | Maximum Burst Frequency   | f <sub>b(max)</sub>  | Use option HFB for >10 pulses within 20µs or less  |   |  |   |  |  | 500                     |                               | kHz        |
|                            | Maximum Number of Pulses / Burst N <sub>(mi</sub>   |  | @ fb(max) Standard  Note: Option HFB requires external buffer capacitors with a voltage Option I-HFB   |   |  |   |  | >10  |                         |                               |            |
|                            |   |  |  |   |  |   |  | >100   |                         |                               |            |
|                            |   |  |  | d a cpacitance of 100nF per additional Option HFB |  |   |  | >1000  |                         | Pulses                        |            |
|                            | Coupling Capacitance C  |  |  | side against control side                         |  |   |  |  | <100                    |                               | pF         |
|                            | Natural Capacitance   | C <sub>N</sub>   | Between switch poles, @ 0.5 x V <sub>O(max)</sub>  |   |  | 9   | 8  | 6  | pF                      |                               |            |
|                            | Control Voltage Range V   |  | The V <sub>ctrl</sub> has no impact on the output pulse shape.   |   |  |   |  | 3 10   |                         | VDC                           |            |
|                            | Auxiliary Supply Voltage Range  | Vaux   |  |   | ired in the HFS m  |   |  |  | 4.5 5.5                 |                               | VDC        |
|                            | Typical Auxiliary Supply Current  | laux   | Vaux = 5.00 VDC, T <sub>case</sub> = 25°C.  0.01 x f <sub>(max)</sub>  |   |  |   | 220  |  | mADC                    |                               |            |
|                            | Fault Signal Output   |  | Active current limitation above 1A. @ f <sub>(max)</sub> Switch will be turn off, if f>f <sub>(max)</sub> , V <sub>aux</sub> <4.75V or T <sub>case</sub> >75°C |   | 600<br>>4.0  |   |  | IIIADC   |                         |                               |            |
|                            | Tault Signal Output   |  | Fault condition is indicated by a logical "L"  |   | <0.8   |   |  | VDC  |                         |                               |            |
|                            | Opt. HFS, Ext. Supply Voltage V   | 1 V <sub>HFS(V1)</sub>   |  |   | irrent consumption <0.4 mA/kHz @ 25°C                          |   |  | 15   |                         |                               |            |
|                            | Opt. HFS, Ext. Supply Voltage V   |  |  |   |  | TBD   |  |  |                         | VDC                           |            |
|                            | Intrinsic Diode Forward Voltage V <sub>F</sub>  |  | T <sub>case</sub> = 25°C, I <sub>F</sub> = 0.3 x I <sub>P(max)</sub>   |   |  |   | <30  |  |                         |                               | VDC        |
|                            | Diode Reverse Recovery Time t <sub>rrc</sub>  |  | $T_{case} = 25$ °C, $I_F = 0.3 \text{ x } I_{P(max)}$ , $di/dt = 100 \text{ A/}\mu\text{s}$  |   |  |   | <50  |  |                         |                               | ns         |
| Al-                        | Dimensions  | LxWxH  | Standard housing   |   |  |   | Please contact the   |  |                         |                               | 1          |
| HOUSING                    |   | Devices with option DLC  |  |   |  | manufactured!                                       |  |  | mm <sup>3</sup>         |                               |            |
|                            | Woight  |  | Standard housing   |   |  |   |  |  |                         |                               |            |
|                            | Weight  |  |  | Standard housing                                  |  |   |  | Please contact the   |                         |                               |            |
|                            |   | Devices with option DLC  |  |   |  |   | manufactured!  |  |                         | g                             |            |
| FUNCTIONS                  | Control Signal Input Pin 1 / Yellow (LS-C: Pin 1). TTL compatible (LS-C: With 100Ω termination). So     |  |  |   |  |   |  | ger characteris  | tics. Control voltage 2 | 2-10 V (3-5 V for low jit     | ter).      |
|                            | Logic GND / 5V Return Pin 2   | C: Shielding). Th  | safety ear   | things termina                                    | ls (threaded inserts) o  | on bottom side.                                     |  |  |                         |                               |            |
|                            | 5V Auxiliary Supply Pin 3 / Red (LS-C: Pin 4). The 5 V input is used for rep rates up to the specified  |  |  |   |  |   |  | uency f <sub>(max).</sub> Hi   | gher rep rates require  | option HFS.                   |            |
| CTI                        | Fault Signal Output Pin 4 / Orange (LS-C: Pin 3). TTL output, short circuit proof. Indicating switch 8  |  |  |   |  |   | driver ove   | r-heat, over-fre   | equency, low auxiliary  | voltage. L = Fault.           |            |
| Z                          | Inhibit Signal Input Pin 5 / Green (LS-C: Pin 2). TTL compatible, Schmitt-Trigger characteristics for   |  |  |   |  |   |  |  |                         |                               |            |
| F                          | LED Indicators GREEN: "Auxiliary power good, switch OFF". YELLOW: "Control signal receiv                |  |  |   |  |   |  |  | •                       |                               |            |
|                            | Temperature Protection Switches with option DLC: 65°C, response time < 3 s @ 3xPd(max), △T=25K (40 to 6 |  |  |   |  |   |  | ant flow > 3I / m  | in. Separate driver pro | tection.                      |            |
| ORDERING                   |   |  |  |   | Option LP Low Pass. Input filter for increased noise immunity. |   |  |  |                         | s according to customer speci | ification. |
|                            | HTS 201-240 SiC Fast HV SiC Mosfet Swit HTS 201-300 SiC Fast HV SiC Mosfet Swit                         |  | Option HFB High Frequency Burst (improved capability by external ca  Option HFS High Frequency Switching (two auxiliary supply inputs V1                       |   |  |   | Option UL-94<br>Option I-FWD   | Flame retardant casting re   |                         | load only                     |            |
|                            | Past HV SIC Mosfet Swit   | Option I-HFS Integrated High Frequency Burst Integrated High Frequency Burst |  |   | W VZ )   | Option I-FWDN                                       |  | ode. In connection with inductive<br>de Network. In connection with in |                         |                               |            |
|                            |   | Option S-TT Soft Transition Time decrease the rise and fall time by 20%      |  |   |  | Option PT-C   | Pigtail for control connection:  | Flexible leads (I=75mm) with len                                       | no                      |                               |            |
| SD                         |   | Option Min-On Individually increased "Min. On-Time" to avoid unwanted to     |  |   |  |   |  | eparate  |                         |                               |            |
| 0                          |   |  | Option Min-Off Individually increased "Min. Off-Time" to avoid unwanted to Option PCC Pulser Configuration. Switch combined with custom speci                  |   |  |   |  |  |                         | tor 3 to 10.                  |            |
|                            |   |  | Option ISO-40  | 40kV Isolation.                                   | Isolation Voltage increa                                       | ased to 120kV.                                      | Option DLC Direct Liquid Cooling. P <sub>d(max)</sub> can be increased by the factor 10 to 100 |  |                         |                               | 0 to 100.  |
|                            |   |  | Option ISO-60  |   |  |   |  |  |                         | ASE REFER TO THE OPTIO        |            |
| Cust                       | tomized switching units are available on re   | equest. All data   | and specifications s   | ubject to chang                                   | e without notice. Plea   | ise vis it www.beh                                  | lke.com for u  | p-dates.   | 201-180-SiC-RS / Re     | evision 12-11-2020 ©2017 A    | III rights |