	Specification		Symbol	Condition / C	omment					151-15-SiC	Uni
	Maximum Operati	ing Voltage	$V_{O(max)}$	I <sub>off</sub> < 50 μADC,	T <sub>case</sub> = 70°C	)		± 15			kVD
	Maximum Isolatio		VI	Between HV sw	vitch and cor	ntrol / GND, conti		1		± 25	kVD
89	Max. Housing Ins	ulation Volta	ige V <sub>INS</sub>	Between switch and housing surface, 3 minutes					± 50	kVD	
•	Maximum Turn-O			T <sub>case</sub> = 25°C		duty cycle <1%				155	
RATIN					t <sub>p</sub> < 1 ms, d	luty cycle <1%				100	
R					t <sub>p</sub> < 10 ms,	duty cycle <1%				65	
M					t <sub>p</sub> < 100 ms, duty cycle <1%					45	ADO
M	Maximum Continuo	ous Load Cur	rrent I <sub>L(max)</sub>		Standard devices					1.26	
X			iz(max)	T <sub>case</sub> = 25°C	T <sub>case</sub> = 25°C Devices with option DLC					16.5	ADO
MAXIMUM	Max. Continuous F	ower Dissipa	ation P <sub>d(max)</sub>	Standard devices & EC, forced air 4 m/s						16	
			T d(max)	T <sub>case</sub> = 25°C		evices with option ILC			1400		Wat
UTE	Linear Derating	r Derating		Standard devices & EC for			ed air 4 m/s			0.11	
	9		Above 25°C Devices with option DLC					13.07	W/h		
4 <i>BSOL</i>	Operating Tempe	rature Range	e T <sub>O</sub>	Standard devices & options CF, GCF, ILC. (Option DLC)				-4070 (60)			
A	Storage Temperature Range		Ts	Switches with option ILC may require frost protection!				-4090			
	Max. Permissible			Homogeneous steady-field, surrounding the whole switch					25	°C mT	
	Max. Auxilliary Voltage		V <sub>aux</sub>	Built-in overvoltage limiter (replaceable)					5.5	VDO	
	Permissible Operating Voltage Range				Julie III overvoltage infliter (replaceable)					0 ± 15	kVD
	Typical Breakdown Voltage		V <sub>br</sub>	NOTE: $V_{br}$ is a test parameter for quality control purposes only. Not applicable in normal operation! $I_{off} > 0.5 \text{ mA}$						16	kVD
			- 51								
	Typical Off-State Current		l <sub>off</sub>		570°C, reduced loff on request					< 80	μAD
	Typical Turn-On F	Resistance	R <sub>stat</sub>	_	Each switching path		0.1 x I <sub>P(max)</sub> , T <sub>case</sub> =25°C		0.55		
				t₀ < 1µs, duty cycle < 1%		1.0 x I <sub>P(max)</sub> , T <sub>case</sub> =25°C			1.16		
						1.0 x I <sub>P(max)</sub> , T <sub>case</sub> =70°C			2,75		
	Typical Propagati		ne t <sub>d(on)</sub>			0.8 x V <sub>O(max)</sub> , 50-5				200	ns
	Typical Output Pulse Jitter		tj		ched input, V <sub>aux</sub> / V <sub>ctrl</sub> = 5.00 VDC					3	ns
	Typical Turn-On Rise Time		t <sub>r(on)</sub>	Resistive load, 10-90% 0.1 x $V_{O(max)}$ , $I_L = 0.1$ x $I_{p(max)}$					13		
							$I_L = 0.1 \times I_{p(max)}$			27	
							$0.8 \times V_{O(max)}$ , $I_L = 1.0 \times I_{p(max)}$			31	ns
S	Typical Turn-Off F	Rise Time	$t_{\text{off}},t_{\text{q}}$	Resistive load,	10-90%		$I_L = 0.1 \times I_{p(max)}$			50	
, C.						0.8 x V <sub>O(max)</sub> , I	$I_L = 1.0 \times I_{p(max)}$			100	ns
ERISTICS	Maximum Turn-On Time		t <sub>on(max)</sub>	No limitation						∞	ns
ERI	Minimum Turn-Or		t <sub>on(min)</sub>	t <sub>on(min)</sub> can be customized. Please consult factory						150	ns
	Maximum Turn-O			No limitation						∞	ns
246	Minimum Turn-Of		toff(max)		stomized. P	lease consult fac	torv			150	ns
AA	Max. Continuous			@ V <sub>aux</sub> = 5.00 V		I devices without				12	
CH	Frequency		·(max)	Sw. shutdown if						40	
7	. 1			f <sub>(max)</sub> is exceeded						80	kHz
ECTRICAL	Maximum Burst F	requency	f <sub>b(max)</sub>				nr less			500	kHz
T.R.	Maximum Number of Pulses / Burst			Use option HFB for >10 pulses within 20µs or less  @ f <sub>b(max)</sub> Standard						>10	
EC	With the transfer of the trans	naximum Number of Fulses / Burst		-	Ontan		Option I-HFB	>100			
EL				Note: Option HFB requires external buffer capacitors with a voltage rating of > 630VDC and a cpacitance of 100nF per additional Option HFB				>1000			Pulse
_	Coupling Capacitance		Cc	HV side against control side				<100			pF
	Natural Capacitance		C <sub>N</sub>	Between switch poles, @ 0.5 x V <sub>O(max)</sub>			200			pF	
	Control Voltage Range		V <sub>ctrl</sub>	The V <sub>ctrl</sub> has no impact on the output pulse shape.					3 10	VDO	
	Auxiliary Supply Voltage Range			The +5 V supply is not required in the HFS mode.				4.5 5.5	VDO		
	Typical Auxiliary Supply Current		,	$V_{\text{aux}} = 5.00 \text{ VDC}$ , $T_{\text{case}} = 25^{\circ}\text{C}$ . $0.01 \text{ x f}_{(\text{max})}$				180			
	Typical Auxiliary Supply Current		laux	Active current limitation above 1A.		800		mAD			
	Fault Signal Output			Switch will be turn off, if f>f <sub>(max)</sub> , V <sub>aux</sub> <4.75V or T <sub>case</sub> >75°C				>4.0			
	. aan oignai outp			Fault condition is indicated by a logical "L"					<0.8		
	Opt. HFS, Ext. Su	ipply Voltage	e V1 V <sub>HFS(V1)</sub>				/kHz @ 25°C	1	15		
					tability ±3%, current consumption <0.4 mA/kHz @ 25°C tability ±3%, current consumption <0.5 mA/kHz @ 25°C				65		
	Opt. HFS, Ext. Supply Voltage V2 Intrinsic Diode Forward Voltage		-( )	T <sub>case</sub> = 25°C, I <sub>F</sub> = 0.3 x I <sub>P(max)</sub>			1	24			
	Diode Reverse Recovery Time			$T_{case} = 25^{\circ}\text{C}, I_F = 0.3 \text{ x } I_{P(max)}$ $T_{case} = 25^{\circ}\text{C}, I_F = 0.3 \text{ x } I_{P(max)}, di/dt = 100 \text{ A/}\mu\text{s}$				<250ns			
	Dimensions		LxWxH	·			175x75x58			ns	
9	LXVV			Devices with option ILC						200x125x56	mm
HOUSING				'				ZOOXTZOXOO			
ž	Weight		•	Standard housing					1600		
HC											
										g	
	Control Signal Input $ $ Pin 1 / Yellow (LS-C: Pin 1). TTL compatible (LS-C: With 100 $\Omega$ termination). Sc							chmitt-Trig	ger characteris	ics. Control voltage 2-10 V (3-	5 V for low jitter).
S	Logic GND / 5V R	Return Pi	n 2 / Black (LS-	C: Shielding). The	e ground pin	is internally con	nected with the	e safety ear	rthings termina	s (threaded inserts) on bottom	side.
VO	5V Auxiliary Supp	ly Pi	n 3 / Red (LS-C:	Pin 4). The 5 V i	input is used	I for rep rates up	to the specified	d max. freq	uency f <sub>(max)</sub> . Hic	gher rep rates require option H	FS.
:11	Fault Signal Output Pin 4 / Orange (LS-C: Pin 3). TTL output, short circuit proof. Indicating switch 8										
FUNCTIONS	Inhibit Signal Input  Pin 5 / Green (LS-C: Pin 2). TTL compatible, Schmitt-Trigger characteristics for										
FU	LED Indicators				wer good, switch OFF". YELLOW: "Control signal receive						
	Temperature Prot		-	DLC: 65°C, response time < 3 s @ 3xPd(max), $\Delta$ T=25K (40 to 6							
	HTS 151-15-SiC Fast HV SiC Mosfet Switch					Low Pass. Input filter for increased noise immunity.			Option I-PC	Integrated part components according	to customer specification
	T GOLLIV GIO MICOIOL OWILL			Option HFB	High Frequency Burst (improved capability by external ca			apacitors)	Option UL-94	Flame retardant casting resin, UL94-V	
9				Option HFS	High Frequency Switching (two auxiliary supply inputs V1				Option I-FWD	Integrated Free-Wheeling Diode. In conne	ction with inductive load only.
IN				Option I-HFS Integrated High Frequency Burst					Option I-FWDN	Integrated Freewheeling Diode Network. In	
ORDERING			Option S-TT	Soft Transition Time decrease the rise and fall time by 20%			blace-i-	Option PT-C	Pigtail for control connection: Flexible leads		
KD.			Option Min-On Option Min-Off	Individually increased "Min. On-Time" to avoid unwanted individually increased "Min. Off-Time" to avoid unwanted in				Option SEP-C Option TH	Separated control unit. Control unit with LE Tubular Housing	:u indicators in a separate	
0			Option Min-Off Option PCC	Pulser Configuration. Switch combined with custom spec			** * *		creased by the factor 3 to 1		
				Option ISO-40 40kV Isolation. Isolation Voltage increased to 120kV.			Option DLC Direct Liquid Cooling. Pd(max) can be increased by the factor 10 to 1			•	
					Option ISO-60 60kV Isolation. Isolation Voltage increased to 120kV.					PRODUCT OPTIONS PLEASE REFER	
				Option 100 to	00111	Totalion Follago more					