	Specification	S	ymbol	Condition / Comment					HTS	901-30 SIC GSN	M HTS 1001-15 SiC GSI	M Unit		
	Maximum Operating Voltage V <sub>O(max)</sub>				ADC, T <sub>case</sub> =					90	100	kVDC		
	Maximum Isolation Voltage	I	Between H	HV switch and	d control /	GND, cont	inuously		± 25					
35	Max. Housing Insulation Volta	ge V <sub>II</sub>	INS	Between s	Between switch and housing surface, 3 minutes						± 50	kVD(		
: MAXIMUM RATINGS	Maximum Turn-On Peak Curre	ent I <sub>P(</sub>	(max)	T <sub>case</sub> =	t <sub>p</sub> < 200 μs,					300	150			
				25°C						190	90	ADC		
					t <sub>p</sub> < 10 ms,					58	26			
					t <sub>p</sub> < 100 ms		e <1%			30	15			
	Maximum Continuous Load Current I <sub>L(max</sub>			T <sub>case</sub> = Standard devices						2.52	1.25	ADC		
	May Out to the Division of the Control of the Contr			25°C Devices with option DLC					-	35	16			
	Max. Continuous Power Dissipation P <sub>d(max)</sub>			T <sub>case</sub> = Standard devices & FC, forced air 4 m/s						80	60	10/-4		
Ę	Linear Derating			25°C Devices with option DLC  Above Standard devices & FC, forced air 4 m/s				:- 4/-		6800	5500	Watt		
7	Linear Derating			25°C Devices with option DLC				ir 4 m/s		0.06 40	0.12 80	l <sub>W/K</sub>		
ABSOLUTE	Operating Temperature Range	e To		Standard devices & options CF, GCF, ILC. (Option DLC)				Intion DLC)			-4075	C°		
¥	Storage Temperature Range Ts			Switches with option ILC may require frost protection!					$\overline{}$	-40/5 -5090				
	Max. Permissible Magnetic Fie			Homogeneous steady-field, surrounding the whole switch					-	25				
	Max. Auxilliary Voltage		aux	Built-in overvoltage limiter (replaceable)					_	5				
	Permissible Operating Voltage Range		0						_	± 90	± 100	kVD0		
	Typical Breakdown Voltage		br	NOTE: V <sub>br</sub> is a test parameter for quality I <sub>off</sub> > 0.5 mA				1 > 0 5 mA		99	110	kVD0		
	Timinal Off Ctata Current			control purposes only. Not applicable in										
	Typical Off-State Current		ff	0.8xV <sub>O</sub> , T <sub>case</sub> =2570°C, reduced l <sub>off</sub> on request			4.0	< 40	μAD(					
	Typical Turn-On Resistance  Typical Propagation Delay Time		stat	t₀ < 1µs, duty cycle < 1% 1.0 x			0.1 x I <sub>P(max)</sub> , T <sub>case</sub> =25°C 1.0 x I <sub>P(max)</sub> , T <sub>case</sub> =25°C 1.0 x I <sub>P(max)</sub> , T <sub>case</sub> =70°C			1.8 3.5	3.9 7.8			
										3.5 7	7.8 15	Ohm		
CTERISTICS			(on)								TBD.	ns		
	Typical Output Pulse Jitter		(UII)	Impedance matched input, V <sub>aux</sub> / V <sub>ctrl</sub> = 5.00 VDC			$\overline{}$		3	ns				
	Typical Turn-On/Off Rise Time		(on)/				201	200	125	113				
	Maximum Turn-On Time		(off)	resistive load, 10-90 %			$ \begin{array}{l} 0.1 \; x \; V_{O(max),} \; I_L = 0.1 \; x \; I_{p(max)} \\ 0.8 \; x \; V_{O(max),} \; I_L = 0.1 \; x \; I_{p(max)} \\ 0.8 \; x \; V_{O(max),} \; I_L = 1.0 \; x \; I_{p(max)} \\ \end{array} $			350	220			
			(OII)							410	260	ns		
			n(max)	No limitation							∞	_		
			n(min)	ton(min) can be customized. Please consult factory				ctory			220	ns		
K	Maximum Turn-Off Time toff(r			No limitation							∞			
	Minimum Turn-Off Time toff(min)			toff(min) can be customized. Please consult factory							220	ns		
	Max. Continuous Switching f <sub>(max)</sub>			@ Vaux= 5.00 V Standard devices without HFS option					on	~2	2.5			
CHARA	Frequency			Sw. shutdown if f <sub>(max)</sub> is Standard devices with HFS supply						50	50			
ECTRICAL CH				exceeded	exceeded Opt. HFS + sufficient cooling option				1	100	100	kHz		
	Maximum Burst Frequency f <sub>b(max)</sub>			Use option HFB for >10 pulses within 20µs or less							2	MHz		
	Maximum Number of Pulses / Burst N <sub>(max)</sub>			@ f <sub>b(max)</sub> Standard							>100 Use option HFB for >150	Pulse		
Ĕ				Note: Option HFB requires external buffer capacitors with a voltage  Option I-HFB				Option I-HFI	В		>1000			
F				, "	/DC and a cpacitano		additional	Option HFB	į.	;	>10000			
E	Coupling Capacitance		c	HV side against control side							<50	pF		
	Natural Capacitance		N	Between switch poles, @ 0.5 x V <sub>O(max)</sub>							<20	pF		
	Control Voltage Range		ctrl	The V <sub>ctrl</sub> has no impact on the output pulse shape.							3 10	VDC		
	Auxiliary Supply Voltage Range		aux	The +5 V supply is not required in the HFS mode. $V_{aux} = 5.00 \text{ VDC}$ , $T_{case} = 25^{\circ}\text{C}$ . $0.01 \text{ x f}_{(max)}$							5	VDC		
	Typical Auxiliary Supply Current		ux					0.01 x f <sub>(max)</sub>	)		>450 800	mAD(		
	Fault Signal Output			Active current limitation above 1A. @ f <sub>(max)</sub>					-+-	<u> </u>	4V, L=0.5V	mAD0 VDC		
	Fault Signal Output			Switch will be turn off, if f>f <sub>(max)</sub> , V <sub>aux</sub> <4.75V or T <sub>case</sub> >75°C Fault condition is indicated by a logical "L"						11-4	+v, L=0.5v	VDC		
	Opt. HFS, Ext. Supply Voltage V1		HFS(V1)	Stability ±3%, current consumption <0.4 mA/kHz @ 25°C				/kHz @ 25°C			15	VDC		
	Opt. HFS, Ext. Supply Voltage V2		HFS(V2)	Stability ±3%, current consumption <0.9 mA/kHz @ 25°C					_		300	VDC		
	Intrinsic Diode Forward Voltage		F	$T_{case} = 25^{\circ}C$ , $I_F = 0.3 \times I_{P(max)}$				7KI 12 (G) 20 'O			126	VDC		
	Diode Reverse Recovery Time		rc	T <sub>case</sub> = 25°C, I <sub>F</sub> = 0.3 x I <sub>P(max)</sub> , di/dt = 100 A/µs					$\overline{}$		<50	ns		
	Dimensions			Standard housing					$\overline{}$	Plea	ase contact the	+		
9				Devices with option CF, non-isolated cooling fins							anufactured!	mm <sup>3</sup>		
8				Devices with option DLC										
HOUSING	Weight			Standard housing						Pleas	ase contact the			
¥				Devices with option CF, non-isolated cooling fins						ma	anufactured!	g		
		Devices with option DLC												
		patible with Schmitt-Trigger characteristics. Control voltage 2-10 V (3-5 V recommended for low jitter).												
	Logic GND / 5V Return Pin 2 / Black. The ground pin is internally connected with the safety earthing terminal (thi									insert) on bottom sid	de.			
Š	5V Auxiliary Supply Pin 3 / Red. The 5 V input is used for rep rates up to the specified max. frequency f <sub>(max)</sub> . I								max). Higher r	ep rates require opt	tion HFS.			
v	Fault Signal Output Pi	tput, short o	out, short circuit proof. Indicating switch & driver over-heat, over-frequency, low auxiliary voltage. L = Fault.											
-	Inhibit Signal Input Pin 5 / Green. TTL compatible, Schmitt-Trigger characteristics for the							e connection of	external safe	ety circuits. L = Swi	itch Inhibited.			
VC7/	LED Indicators GI	er good, switch OFF". YELLOW: "Control signal received, switch ON". RED: "Fault condition, switch OFF"												
FUNCTIONS		Standard swite	tches ar	nd switches	with option C	F, GCF: Th	nermo trigg	er 75°C, respons	se time < 60	e < 60 s @ $3xPd(max)$ , $\Delta T=25K$ (50 to $75^{\circ}C$ ). Separate driver				
FUNCTI	Temperature Protection A)				n DLC: 65°C, response time < 3 s @ $3xPd(max)$ , $\Delta T=25K$ (40									
FUNCTI	1 .	otection. <b>B)</b> Swi					sed noise imm		Option CCS	on CCS Ceramic Cooling Surface. Pd(max) can be increased by the factor 2 to 3.				
_	Pro HTS 901-30 SiC GSM Transistor Sw	vitch, 90 kVDC, 300	ADC 0									• • • • • • • • • • • • • • • • • • • •		
_	Pro HTS 901-30 SiC GSM Transistor Sw		ADC O	ption S-TT	Soft Transition Tir	ne. Slower sw		for simplified EMC.	Option CCF		ng. P <sub>d(max)</sub> can be increased by the factor			
_	Pro HTS 901-30 SiC GSM Transistor Sw	vitch, 90 kVDC, 300	ADC O	ption S-TT S	Soft Transition Tir High Frequency B	me. Slower sw Burst, Improve	d burst capabi	for simplified EMC. lity by driver.	Option CF	Copper Cooling Fins. Po	ng. $P_{d(max)}$ can be increased by the factor $P_{d(max)}$ can be increased by the factor 3 to	o 10.		
_	Pro HTS 901-30 SiC GSM Transistor Sw	vitch, 90 kVDC, 300	ADC 0	ption S-TT S ption HFB I	Soft Transition Tir High Frequency B	me. Slower sw Burst, Improve Switching (two	d burst capabi auxiliary supp	for simplified EMC. lity by driver. ly inputs V1 & V2 )		Copper Cooling Fins. Po Grounded Cooling Flange	ng. P <sub>d(max)</sub> can be increased by the factor	o 10. factor 3 to 15.		
ORDERINGTI FUNCTI	Pro HTS 901-30 SiC GSM Transistor Sw	vitch, 90 kVDC, 300	ADC 0	ption S-TT S ption HFB I ption HFS I ption UFTR I	Soft Transition Tir High Frequency B High Frequency S Ultra Fast Thermo	me. Slower sw Burst, Improve Switching (two otrigger. Resp	d burst capabi auxiliary supp onse time for s	for simplified EMC. lity by driver. ly inputs V1 & V2 )	Option CF Option GCF	Copper Cooling Fins. Po Grounded Cooling Flange Indirect Liquid Cooling (for	ng. $P_{d(max)}$ can be increased by the factor $P_{d(max)}$ can be increased by the factor 3 to e (copper). $P_{d(max)}$ can be increased by the factor 3 to	o 10. factor 3 to 15. ctor 3 to 15.		