	Specification	Symbol	Condition / C	omment			1001-120-LC2 Unit			
	Maximum Operating Voltage	V _{O(max)}	I _{off} < 40 μADC, T _{case} = 70°C				± 100 kVDC			
	Maximum Isolation Voltage	V _I	Between HV switch and control / GND, continuously				± 180 kVDC			
S	Max. Housing Insulation Voltage	V _{INS}	Between RV switch and control / GND, continuously Between switch and housing surface, 3 minutes					± 200	kVDC	
RATINGS	Maximum Turn-On Peak Current	I _{P(max)}	$T_{case} = 25^{\circ}C$ $t_o < 200 \ \mu s$, duty cycle <1%					1200	RVBO	
	Waximum Tum On Fock Gunent	iP(max)	T case 20 0						708	
\$				t _p < 1 ms, duty cycle <1% t _o < 10 ms, duty cycle <1%				396		
				t_p < 100 ms, duty cycle <1%					233	ADC
MAXIMUM	Maximum Continuous Load Current			-						7180
3	Maximum Conunuous Load Current	I _{L(max)}	T _{case} = 25°C	Standard devices					5	ADC
3	M. Outin B. Brandin			Devices with option DLC					37	ADC
	Max. Continuous Power Dissipation	$P_{d(\text{max})}$	T _{case} = 25°C Standard devices & FC, forced air 4 m/s					150	\A/-11	
Ë					Devices with option DLC Standard devices & FC, forced air 4 m/s				3500	Watt
4BSOLUTE	Linear Derating		Above 25°C Standard devices & PC, forced all 4 m/s Devices with option DLC						3.72	14/1/2
SO	Overting Transport of Breeze	-	Standard devices & options CF, GCF, ILC. (Option DLC)						354,81	W/K °C
8	Operating Temperature Range	T ₀	, , , , , , , , , , , , , , , , , , , ,						-4070 (60)	_
•	Storage Temperature Range	Ts	Switches with option ILC may require frost protection!						-4090	°C
	Max. Permissible Magnetic Field	В	Homogeneous steady-field, surrounding the whole switch						25	mT
	Max. Auxilliary Voltage	V _{aux}	Built-in overvoltage limiter (replaceable)						5.5	VDC
	Permissible Operating Voltage Range	Vo							0 ± 100	kVDC
	Typical Breakdown Voltage	V _{br}		NOTE: V _{br} is a test parameter for quality control purposes only. Not applicable in normal operation!				110		
	Typical Off-State Current	l _{off}	0.8xV _O , T _{case} =2570°C, reduced I _{off} on request						< 40	μADC
	Typical Turn-On Resistance R _{stat}		Each switching path		0.1 x I _{P(max)} , T _{case} =25°C				2.5	μποσ
	. Jpiour ruin On Nooistanoo	i soldi	ŭ.			1.0 x I _{P(max)} , T _{case} =25°C			10	
					1.0 x I _{P(max)} , T _{case} =70°C		29.6		Ohm	
	Typical Propagation Delay Time	t _{d(on)}	Resistive load	Resistive load, 0.1 x I _{P(max)} , 0.8 x V _{O(max)} , 50-50%				250		
	Typical Output Pulse Jitter	Resistive load, 0.1 x $I_{P(max)}$, 0.8 x $V_{O(max)}$, 50-50% Impedance matched input, $V_{aux} / V_{ctrl} = 5.00 \text{ VDC}$							ns ns	
	Typical Turn-On Rise Time	t _j		Resistive load, 10-90% $ 0.1 \times V_{\text{O(max)}} $ $ L = 0.1 \times I_{\text{p(max)}} $					250	119
CHARACTERISTICS	Typical Fulli-Off Rise Time	, ,	Resistive load,	10-30 /0					200	
					0.8 x $V_{O(max)}$, $I_L = 0.1$ x $I_{p(max)}$ 0.8 x $V_{O(max)}$, $I_L = 1.0$ x $I_{p(max)}$				350	ns
	Tunical Turn Off Disc Time		Desistive lead	10.000/	$0.1 \times V_{O(max)}$, $I_L = 0.1 \times I_{p(max)}$				100	113
	Typical Turn-Off Rise Time	t _{off} , t _q	Resistive load, 10-90%		$0.8 \times V_{O(max)}$, $I_L = 0.1 \times I_{p(max)}$				150	20
									ns	
	Maximum Turn-On Time	t _{on(max)}	No limitation					∞	ns	
Ē	Minimum Turn-On Time	t _{on(min)}	ton(min) can be customized. Please consult factory					250	ns	
2	Maximum Turn-Off Time	t _{off(max)}	No limitation						∞	ns
HARA	Minimum Turn-Off Time	$t_{\text{off(min)}}$	t _{off(min)} can be customized. Please consult factory						250	ns
	Max. Continuous Switching	$f_{(max)}$	@ V _{aux} = 5.00 V Standard devices without HFS option						<1.0	
_	Frequency		Sw. shutdown if		devices with HF				30	
ELECTRICAL			f _(max) is exceeded Opt. HFS + sufficient cooling option							kHz
3	Maximum Burst Frequency f _{b(max)} Maximum Number of Pulses / Burst N _(max)		Use option HFB for >10 pulses within 20µs or less						400	kHz
E			@ f _{b(max)} Standard			>10				
Į.			Note: Option HFB requires external buffer capacitors with a voltage Option I-HFB						>100	
Ш			rating of > 630VDC and a cpacitance of 100nF per additional Option HFB							Pulses
	Coupling Capacitance	Cc	HV side against control side							pF
	Natural Capacitance C _N		Between switch poles, @ 0.5 x V _{O(max)}				10 5			pF
	Control Voltage Range V _{ctrl}		The V _{ctrl} has no impact on the output pulse shape.				3 10			VDC
	Auxiliary Supply Voltage Range Val		The +5 V suppl	y is not requi	ired in the HFS m	node.	4.5 5.5			VDC
	Typical Auxiliary Supply Current	laux	V _{aux} = 5.00 VD0	V _{aux} = 5.00 VDC, T _{case} = 25°C.		0.01 x f _(max)	400		400	
			Active current limitation above 1A.			@ f _(max)	800			mADC
	Fault Signal Output			Switch will be turn off, if f>f _(max) , V _{aux} <4.75V or T _{case} >75°C				>4.0		
				Fault condition is indicated by a logical "L"			<0.8			VDC
HOUSING	Opt. HFS, Ext. Supply Voltage V1 V _H		Stability ±3%, current consumption <0.4 mA/kHz @ 25°C			15			VDC	
	Opt. HFS, Ext. Supply Voltage V2 V _{HFS(V2)}		Stability ±3%, current consumption <0.5 mA/kHz @ 25°C				TBD			VDC
	Intrinsic Diode Forward Voltage V _F		$T_{case} = 25^{\circ}C, I_F = 0.3 \text{ x } I_{P(max)}$					120		
	Diode Reverse Recovery Time	$T_{case} = 25$ °C, $I_F = 0.3 \text{ x } I_{P(max)}$, $di/dt = 100 \text{ A/}\mu\text{s}$					<250ns			
	Dimensions	t _{rrc} LxWxH							Please contact the	ns
		Devices with option DLC					manufactured!		mm ³	
		·								
	Weight	Standard housing						Please contact the		
Ĭ		Devices with or	Devices with option DLC				manufactured!			
	One-bad Circuit Pin 4 Valle (LO O Div 4) TTI 100 O Div 4 100 O Div 5 100 O Div 6 100						hmitt T==	nor characteris	tios Control voltogo 2 40 V/2 5 V 5	low jitter\
FUNCTIONS	Control Signal Input Pin 1 / Yellow (LS-C: Pin 1). TTL compatible (LS-C: With 100Ω termination). Sch							-		ow juter).
	Logic GND / 5V Return Pin 2 / Black (LS-C: Shielding). The ground pin is internally connected with the sai							•	,	
	5V Auxiliary Supply Pin 3 / Red (LS-C: Pin 4). The 5 V input is used for rep rates up to the specified									
12	Fault Signal Output Pin 4 / Orange (LS-C: Pin 3). TTL output, short circuit proof. Indicating switch &									
3	Inhibit Signal Input Pin 5 / Green (LS-C: Pin 2). TTL compatible, Schmitt-Trigger characteristics for						· · · · · · · · · · · · · · · · · · ·			
W.	LED Indicators GREEN: "Auxiliary power good, switch OFF". YELLOW: "Control signal received and the control signal received and							ON". RED : "F	Fault condition, switch OFF"	
	Temperature Protection Switches with option DLC: 65°C, response time < 3 s @ 3xPd(max), ΔT=25K (40 to 6						65°C), cool	ant flow > 3I / m	in. Separate driver protection.	
	HTS 1001-120-LC2 Fast HV Mosfet Switch,						Option I-PC	Integrated part components according to custome	er specification.	
		Option HFB High Frequency Burst (improved capability by external cap					Option UL-94	Flame retardant casting resin, UL94-V0		
9		Option HFS High Frequency Switching (two auxiliary supply inputs V1 Option I-HFS Integrated High Frequency Burst			& V2)	Option I-FWD Option I-FWDN	Integrated Free-Wheeling Diode. In connection with in			
8			Option I-HFS Option S-TT			d fall time by 20%		Option PT-C	Integrated Freewheeling Diode Network. In connection Pigtail for control connection: Flexible leads (I=75mm)	
ORDERING		Option S-TT Soft Transition Time decrease the rise and fall time by 20% Option Min-On Individually increased "Min. On-Time" to avoid unwanted			triggering	Option SEP-C	Separated control unit. Control unit with LED indicator			
2R		Option Min-Off			triggering Option TH Tubular Housing					
		Option PCC Pulser Configuration. Switch combined with custom speci								
		Option ISO-120 120kV Isolation. Isolation Voltage increased to 120kV.				Option DLC	Direct Liquid Cooling. P _{d(max)} can be increased by the PRODUCT OPTIONS PLEASE REFER TO THE			
Cust	omized switching units are available on regu	Option ISO-200 200kV Isolation. Isolation Voltage increased to 200kV. Index specifications subject to change without notice. Please visit www.behlk				lke.com.for.u		1001-120-LC2-RS / Revision 08-03-2024		
100	James a land of the country of the c									