	Specification	Symbol	bol Condition / Comment				1001-20-L0	C2 1201-2	0-LC2 1	401-20-LC2	1501-20-LC2	Unit	
	Maximum Operating Voltage	$V_{O(max)}$	I <sub>off</sub> < 50 μADC,			± 100	± 1	20	± 140	± 150	kVDC		
	Maximum Isolation Voltage	VI	• •									kVDC	
RATINGS	Max. Housing Insulation Voltage	V <sub>INS</sub>	Between switch and housing surface, 3 minutes									kVDC	
	Maximum Turn-On Peak Current	I <sub>P(max)</sub>	T <sub>case</sub> = 25°C		, duty cycle <1%		100						
		· (max)	0000		t <sub>p</sub> < 1 ms, duty cycle <1%				118				
8					duty cycle <1%			72					
2				t <sub>p</sub> < 100 ms, duty cycle <1%					56	56		ADC	
3	Maximum Continuous Load Current	I <sub>L(max)</sub>		Standard devices			1.26					<del> </del>	
×	Maximum Continuous Essas Current	IL(IIIdA)	T <sub>case</sub> = 25°C	Devices with option DLC				16.5					
MAXIMUM	Max. Continuous Power Dissipation	P <sub>d(max)</sub>	Standard devices & EC forced air 1 m/s			120	15		160	180	ADC		
	μ	- u(max)	T <sub>case</sub> = 25°C	Devices with option DLC		3000	320		3300	3500	Watt		
4BSOLUTE	Linear Derating		41 0500	Standard devices & FC, forced air 4 m/s			2.84	3.	4	3.72	4.01		
70	ű		Above 25°C Devices with option DLC				237,5	307	,31	354,81	393.16	W/K	
BS	Operating Temperature Range	To	Standard devices & options CF, GCF, ILC. (Option DLC)					-4070 (	(60)		°C		
4	Storage Temperature Range	Ts	Switches with option ILC may require frost protection!				-4090				°C		
	Max. Permissible Magnetic Field	В	Homogeneous steady-field, surrounding the whole switch				25					mT	
	Max. Auxilliary Voltage	Built-in overvoltage limiter (replaceable)				5.5					VDC		
	Permissible Operating Voltage Range					0 ± 100 0 ± 120 0 ± 140 0 ± 150				kVDC			
	Typical Breakdown Voltage	V <sub>br</sub>	NOTE: V <sub>br</sub> is a test parameter for quality control				110	13	2	148	164	kVDC	
	Typical Off-State Current	l <sub>off</sub>	purposes only. Not applicable in normal operation! 1007 U.S. IIIA 0.8xVo, T <sub>case</sub> =2570°C, reduced l <sub>off</sub> on request					< 40			μADC		
	Typical Turn-On Resistance	R <sub>stat</sub>	Each switching path 0.1 x I <sub>P(max)</sub> , T <sub>case</sub> =25°C				20	2		26	29.1	μλυσ	
	Typical rum-on Resistance	i Vstat			1.0 x I <sub>P(max)</sub> , T <sub>case</sub> =25°C		40	41		51.6	57.7		
			φ · 1μ3, duty cyclc · 170		1.0 x I <sub>P(max)</sub> , T <sub>case</sub> =70°C		99.4	103		129.8	143.4	Ohm	
	Typical Propagation Delay Time	t <sub>d(on)</sub>	Resistive load, 0.1 x I <sub>P(max)</sub> , 0.8 x V <sub>O(max)</sub> , 50-50%			-		250			ns		
	Typical Output Pulse Jitter	Impedance matched input, V <sub>aux</sub> / V <sub>ctrl</sub> = 5.00 VDC				3					ns		
	Typical Turn-On Rise Time	t <sub>j</sub>	Resistive load, 10-90% $0.1 \times V_{\text{c(max)}}$ , $I_{\text{L}} = 0.1 \times I_{\text{p(max)}}$				16	20		15	14	<u> </u>	
	Typical Fairi Sirrilos Tillo		r toolouvo loaa,	10 00 70	0.8 x V <sub>O(max)</sub> , I <sub>L</sub>			92		62	64		
TERISTICS					0.8 x V <sub>O(max)</sub> , I <sub>L</sub>		90	10		110	115	ns	
	Typical Turn-Off Rise Time		Resistive load,	10-90%	$0.1 \times V_{O(max)}$ , $I_L = 0.1 \times I_{p(max)}$				50				
	71		,		$0.8 \times V_{O(max)}$ , $I_L = 1.0 \times I_{p(max)}$				100			ns	
	Maximum Turn-On Time	t <sub>on(max)</sub>	No limitation				∞					ns	
	Minimum Turn-On Time	t <sub>on(min)</sub>						250					
Ĭ	Maximum Turn-Off Time	t <sub>off(max)</sub>	No limitation				∞					ns ns	
CHARAC	Minimum Turn-Off Time	t <sub>off(min)</sub>							250			ns	
	Max. Continuous Switching	f <sub>(max)</sub>	@ V <sub>aux</sub> = 5.00 V Standard devices without HFS option				1.0	0.		0.5	0.5	1.0	
	Frequency	·(IIIdx)	Sw. shutdown if  Standard devices with HFS supply  Opt. HFS + sufficient cooling option				50	5(		50	50		
7							80	80		80	80	kHz	
ELECTRICAL	Maximum Burst Frequency	Use option HFB for >10 pulses within 20µs or less						500			kHz		
78	Maximum Number of Pulses / Burst	N <sub>(max)</sub>	@ f <sub>b(max)</sub> Standard				>10						
EC			Note: Option HFB requires external buffer capacitors with a voltage Option I-HFB				>100						
<b>13</b>			rating of > 630VDC and a cpacitance of 100nF per additional Option HFB									Pulses	
	Coupling Capacitance	HV side against control side				<100					pF		
	Natural Capacitance C <sub>N</sub>		Between switch poles, @ 0.5 x V <sub>O(max)</sub>				10 5					pF	
	Control Voltage Range V <sub>ctrl</sub>		The V <sub>ctrl</sub> has no impact on the output pulse shape.				3 10					VDC	
	Auxiliary Supply Voltage Range	V <sub>aux</sub>		The +5 V supply is not required in the HFS mode.			4.5 5.5					VDC	
	Typical Auxiliary Supply Current	l <sub>aux</sub>		$V_{aux} = 5.00 \text{ VDC}, T_{case} = 25^{\circ}\text{C}.$ 0.01 x f <sub>(max)</sub>		0.01 x f <sub>(max)</sub>	300	33		370	390		
	5 1101 10 1			ctive current limitation above 1A.								mADC	
	Fault Signal Output		Switch will be turn off, if f>f <sub>(max)</sub> , V <sub>aux</sub> <4.75V or T <sub>case</sub> >75°C Fault condition is indicated by a logical "L"			>4.0							
	Ont LICC Est Commiss Valley as 374	V <sub>HFS(V1)</sub>			<u> </u>	U- @ 05°0	<0.8 VDC						
			•	•	on <0.4 mA/kHz @ 25°C		TB	15	553	592	VDC VDC		
	1 11 3 3 11 7		Stability ±3%, current consumption <0.5 mA/kHz @ 25°C				404						
	Intrinsic Diode Forward Voltage Diode Reverse Recovery Time	V <sub>F</sub>	$T_{case} = 25^{\circ}C$ , $I_F = 0.3 \times I_{P(max)}$ $T_{case} = 25^{\circ}C$ , $I_F = 0.3 \times I_{P(max)}$ , $di/dt = 100 \text{ A/}\mu\text{s}$				80	95	<250n	110	113	VDC ns	
	Dimensions	Standard housing					Please contact the						
HOUSING	Dimonologic	LxWxH	Devices with option DLC						Please conta manufactu			mm <sup>3</sup>	
		Sevices with option BEO						manulaciu	ieu:				
	Weight Standard housing						Please contact the						
Ĭ			Devices with option DLC				manufactured!					Kg	
	Control Signal Input    Din 1 / Valley // S. C. Din 1\ TTL compatible // S. C. With 1000 to minution   C. C. Din 1\ TTL compatible // S. C. With 1000 to minution   C. C. Din 1\ TTL compatible // S. C. With 1000 to minution   C. C. Din 1\ TTL compatible // S. C. With 1000 to minution   C. C. Din 1\ TTL compatible // S. C. With 1000 to minution   C. C. Din 1\ TTL compatible // S. C. With 1000 to minution   C. C. Din 1\ TTL compatible // S. C. With 1000 to minution   C. C. Din 1\ TTL compatible // S. C. With 1000 to minution   C. C. Din 1\ TTL compatible // S. C. With 1000 to minution   C. C. Din 1\ TTL compatible // S. C. With 1000 to minution   C. C. Din 1\ TTL compatible // S. C. With 1000 to minution   C. C. Din 1\ TTL compatible // S. C. With 1000 to minution   C. C. Din 1\ TTL compatible // S. C. With 1000 to minution   C. C. Din 1\ TTL compatible // S. C. With 1000 to minution   C. C. Din 1\ TTL compatible // S. C. With 1000 to minution   C. C. Din 1\ TTL compatible // S. C. With 1000 to minution   C. C. Wi						hmitt-Trigger characteristics. Control voltage 2 10 V /2 5 V for low "#6						
FUNCTIONS	Control Signal Input   Pin 1 / Yellow (LS-C: Pin 1). TTL compatible (LS-C: With 100Ω termination). Schmitt-Trigger characteristics. Control voltage 2-10 V (3-5 V for low jitt										⊌I).		
	Logic GND / 5V Return  Pin 2 / Black (LS-C: Shielding). The ground pin is internally connected with the safety earthings terminals (threaded inserts) on bottom side.												
	5V Auxiliary Supply  Pin 3 / Red (LS-C: Pin 4). The 5 V input is used for rep rates up to the specified												
	Fault Signal Output  Pin 4 / Orange (LS-C: Pin 3). TTL output, short circuit proof. Indicating switch &												
	Inhibit Signal Input  Pin 5 / Green (LS-C: Pin 2). TTL compatible, Schmitt-Trigger characteristics for LED Indicators  GREEN: "Auxiliary power good, switch OFF". YELLOW: "Control signal receiv								-				
4													
		DLC: 65°C, response time < 3 s @ 3xPd(max), $\Delta$ T=25K (40 to 0											
ORDERING	HTS 1001-20-LC2 Fast HV Mosfet Switch, 1 HTS 1201-20-LC2 Fast HV Mosfet Switch, 1	Option LP Low Pass. Input filter for increased noise immunity.  Option HFB High Frequency Burst (improved capability by external ca			Option I-PC Integrated part components according to customer specifical pacitors) Option UL-94 Flame retardant casting resin, UL.94-V0					ication.			
	HTS 1201-20-LC2 Fast HV Mosfet Switch, 1 HTS 1401-20-LC2 Fast HV Mosfet Switch, 1	Option HFB High Frequency Burst (improved capability by external ca Option HFS High Frequency Switching (two auxiliary supply inputs V1							<u> </u>	94-V0 connection with inductive l	oad only		
	HTS 1501-20-LC2 Fast HV Mosfet Switch, 1	Option I-HFS Integrated High Frequency Burst					_		ork. In connection with inc				
		Option S-TT Soft Transition Time decrease the rise and fall time by 20%			Option PT-C Pigtail for control connection: Flexible leads (I=75mm) with lemo								
		Option Min-On Individually increased "Min. On-Time" to avoid unwanted					•		rith LED indicators in a sep	parate			
0		Option Min-Off Individually increased "Min. Off-Time" to avoid unwanted Option PCC Pulser Configuration. Switch combined with custom speci Option ISO-120 120kV Isolation. Isolation Voltage increased to 120kV.							or 3 to 10				
					Option DLC Direct Liquid Cooling. Pd(max) can be increased by the fac								
		Option ISO-200 200kV Isolation. Isolation Voltage increased to 200kV.			FC	OR FURTHER PR	ODUCT OPT	IONS PLEASE RE	FER TO THE OPTION	NS PAGE.			
Cust	omized switching units are available on reque	est. All data a	and specifications su	bject to change	without notice. Pleas	se visit www.beh	lke.com for up-da	ates.	1001-20-	LC2-RS / Revision	13-01-2019 ©2012 A	Il rights	